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1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.

2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.

3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

Thu Apr 17 17:11:44 2014

N61 CARRIER BUILD

PDF PAGE

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5253

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NAND BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
335S0998	1	NAND, 19NM, 16GX8, MLC, PPN1.5	U0604	CRITICAL	NAND_16G
335S0993	1	NAND, 19NM, 32GX8, MLC, PPN1.5	U0604	CRITICAL	NAND_32G
335S0994	1	NAND, 19NM, 64GX8, MLC, PPN1.5	U0604	CRITICAL	NAND_64G
335S00010	1	NAND, 19NM, 128GX8, TLC, PPN1.5	U0604	CRITICAL	NAND_128G
138S0867	1	CAP,XSR,10UF,20%,6.3V,0.65MM,HRTZ,0402	C0610,C0611,C0614,C0634	CRITICAL	NAND_16G
138S0867	1	CAP,XSR,10UF,20%,6.3V,0.65MM,HRTZ,0402	C0613,C0633,C0610,C0611,C0614,C0634	CRITICAL	NAND_32G & NAND_64G
138S00003	1	CAP,XSR,15UF,20%,6.3V,0.65MM,HRTZ,0402	C0613,C0633,C0610,C0611,C0614,C0634	CRITICAL	NAND_128G

ALTERNATE NAND BOM OPTIONS

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
335S0992	335S0998	ALTERNATE	J0604	TOSHIBA,NAND,16GB
335S1038	335S0998	ALTERNATE	J0604	HYNIX,NAND,16GB
335S1040	335S0994	ALTERNATE	J0604	HYNIX,NAND,64GB
335S00014	335S0994	ALTERNATE	J0604	TOSHIBA,NAND,64GB
335S00015	335S00010	ALTERNATE	J0604	TOSHIBA,NAND128GB
335S00009	335S0994	ALTERNATE	J0604	SANDISK,NAND,64GB,TLC

SHIELD BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
604-00241	1	SUBASSY, SHIELD, UPPER FRONT, N61	SH2501	CRITICAL	COMMON
604-00242	1	SUBASSY, SHIELD, LOWER FRONT, N61	SH2502	CRITICAL	COMMON
604-00243	1	SUBASSY, SHIELD, LOWER BACK, N61	SH2504	CRITICAL	COMMON
604-00244	1	SUBASSY, SA SHIELD, N61	SH2506	CRITICAL	COMMON

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0002727241

ENGINEERING RELEASED

2014-04-18

N61 BOM CALLOUTS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-9903	1	SCH, MLB, N61	SCH	CRITICAL	?
820-3486	1	PCBF, MLB, N61	PCB	CRITICAL	?
825-6838	1	EEEE FOR 639-4237 16GB	EEEE_G16T	CRITICAL	EEEE_16G
825-6838	1	EEEE FOR 639-5838 32GB	EEEE_G16R	CRITICAL	EEEE_32G
825-6838	1	EEEE FOR 639-5839 64GB	EEEE_G16Q	CRITICAL	EEEE_64G
825-6838	1	EEEE FOR 639-00025 128GB	EEEE_G16N	CRITICAL	EEEE_128G
825-6838	1	EEEE FOR 639-00208 16GB	EEEE_F98F	CRITICAL	EEEE_16G_TDDLTE
825-6838	1	EEEE FOR 639-00209 32GB	EEEE_FQK0	CRITICAL	EEEE_32G_TDDLTE
825-6838	1	EEEE FOR 639-00210 64GB	EEEE_FQJY	CRITICAL	EEEE_64G_TDDLTE
825-6838	1	EEEE FOR 639-00212 128GB	EEEE_FY9W	CRITICAL	EEEE_128G_TLC_TDDLTE

ALTERNATE BOM OPTIONS

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
152S1844	152S1836	ALTERNATE	L1604	TY ALT INDUCTOR
152S1842	152S1849	ALTERNATE	L1519	TY ALT INDUCTOR
197S0392	197S0369	ALTERNATE	Y1200	ESRON ALT XTAL
197S0399	197S0369	ALTERNATE	Y1200	NDK ALT XTAL
338S1285	338S1202	ALTERNATE	U1601	L21 SPKAMP
152S2034	152S2033	ALTERNATE	L1209,L1211,L1213	1.2MM 1.0UH, CYNTEC
152S00004	152S2049	ALTERNATE	L1210,L1212,L1214	1.2MM 0.47UH, CYNTEC
339S00005	339S0246	ALTERNATE	U0201	FIJI, B0, SAMSUNG
339S0247	339S0246	ALTERNATE	U0201	FIJI, B0, HYNIX
339S00006	339S0246	ALTERNATE	U0201	FIJI, B1, E
339S00007	339S0246	ALTERNATE	U0201	FIJI, B1, H
339S00008	339S0246	ALTERNATE	U0201	FIJI, B1, S
155S0773	155S0453	ALTERNATE		TY 120OHM FERRITE
118S0764	118S0717	ALTERNATE	R1309	3.92KOHM, 01005
343S0688	343S0638	ALTERNATE	U2401	CUMULUS C1, FAB4
138S00005	138S00003	ALTERNATE	C1290	15UF,0402,HRTZL CAP
155S00011	155S00008	ALTERNATE	L1135	CMC,90OHM,MURATA
377S0168	377S0140	ALTERNATE	DZ1113	SUPPL TRANS,VARIABLE,AMOTECH
155S0885	155S0610	ALTERNATE	FL1802,FL1803	FERR RD,150OHM,100MA,01005
138S0648	138S0652	ALTERNATE	C1018	CAP,4.7UF,20%,6.3V,0402,R=0.65MM
138S0657	138S0702	ALTERNATE	C1106	CAP,4.3UF,20%,4V,0410
338S00028	338S00017	ALTERNATE	U2203	CARBON, BOSCH, BMT162BC
338S00029	338S00017	ALTERNATE	U2203	CARBON, ST, AP6DE2AA
335S00013	335S0894	ALTERNATE	J0301	RT 8K RESRON

SCH 051-9903

BRD 820-3486

MCO 056-6825

BOM 639-4237 (16GB,BETTER)

BOM 639-5838 (32GB,BEST)

BOM 639-5839 (64GB,ULTRA)

BOM 639-00025(128GB,SUPREME,TLC)

BOM 639-00212(128GB,SUPREME,TLC,DTD)

BOM 639-4237 (16GB,BETTER,DTD)

BOM 639-00209 (32GB,BEST,DTD)

BOM 639-00210 (64GB,ULTRA,DTD)

DRAWING TITLE

SCHEM,MLB,N61

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051-9903

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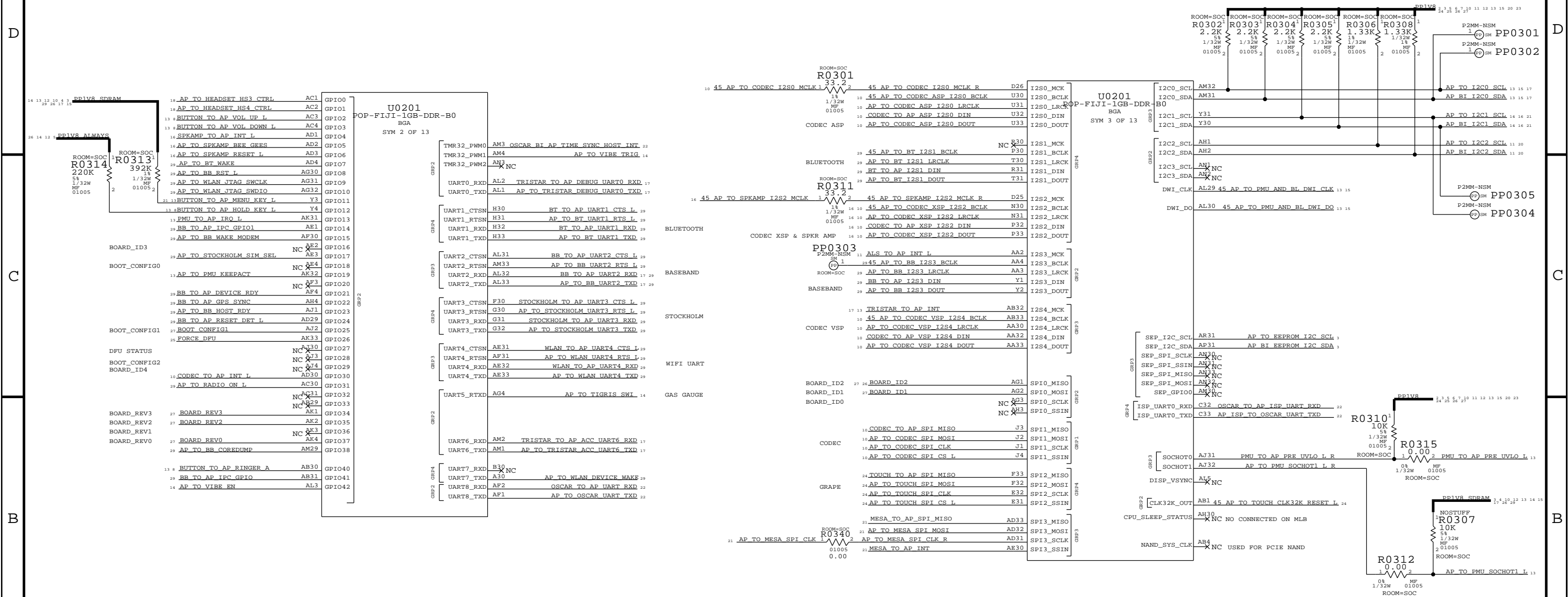
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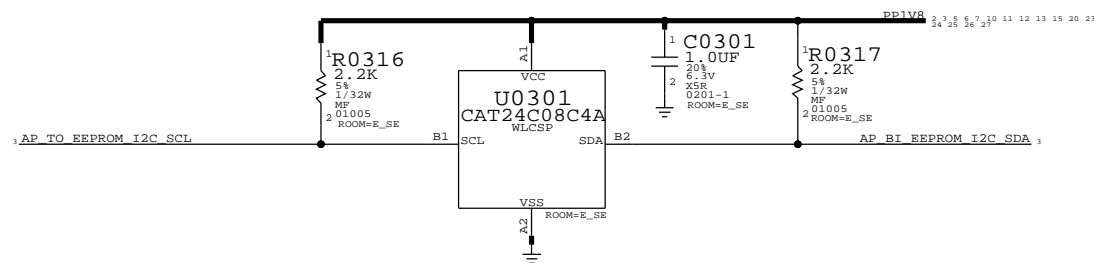
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AD

FIJI: DIGITAL I/O, BOOTSTRAPPING



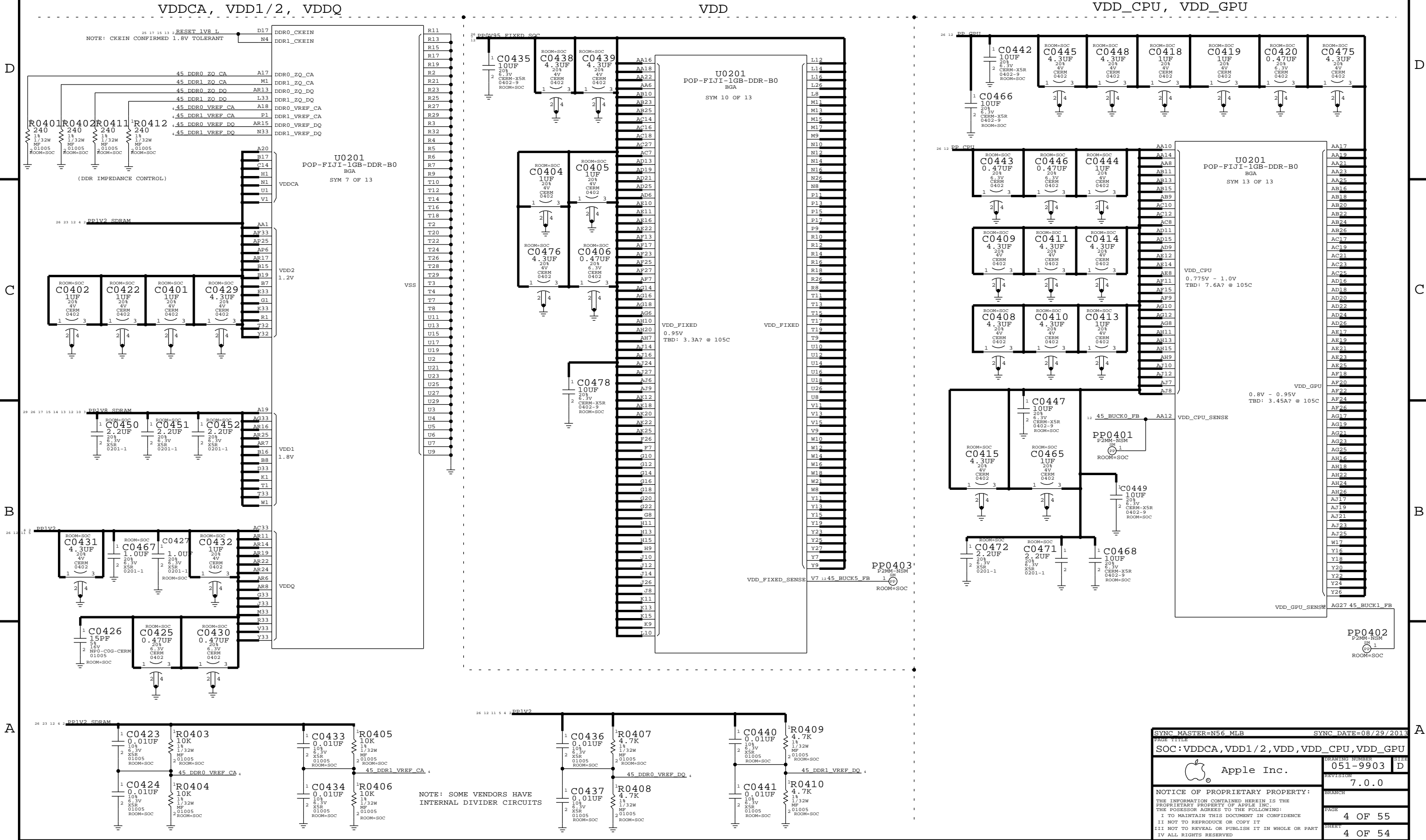
ANTI-ROLLBACK EEPROM
ONSEMI EEPROM
APN: 335S0894



REMOVED HOLD + MENU KEY
BUFFERS SINCE NOT NEEDED FOR FIJI

SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
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FIJI: VDDCA, VDD1/2, VDDQ, VDD, VDD_FIXED, VDD_CPU, VDD_GPU

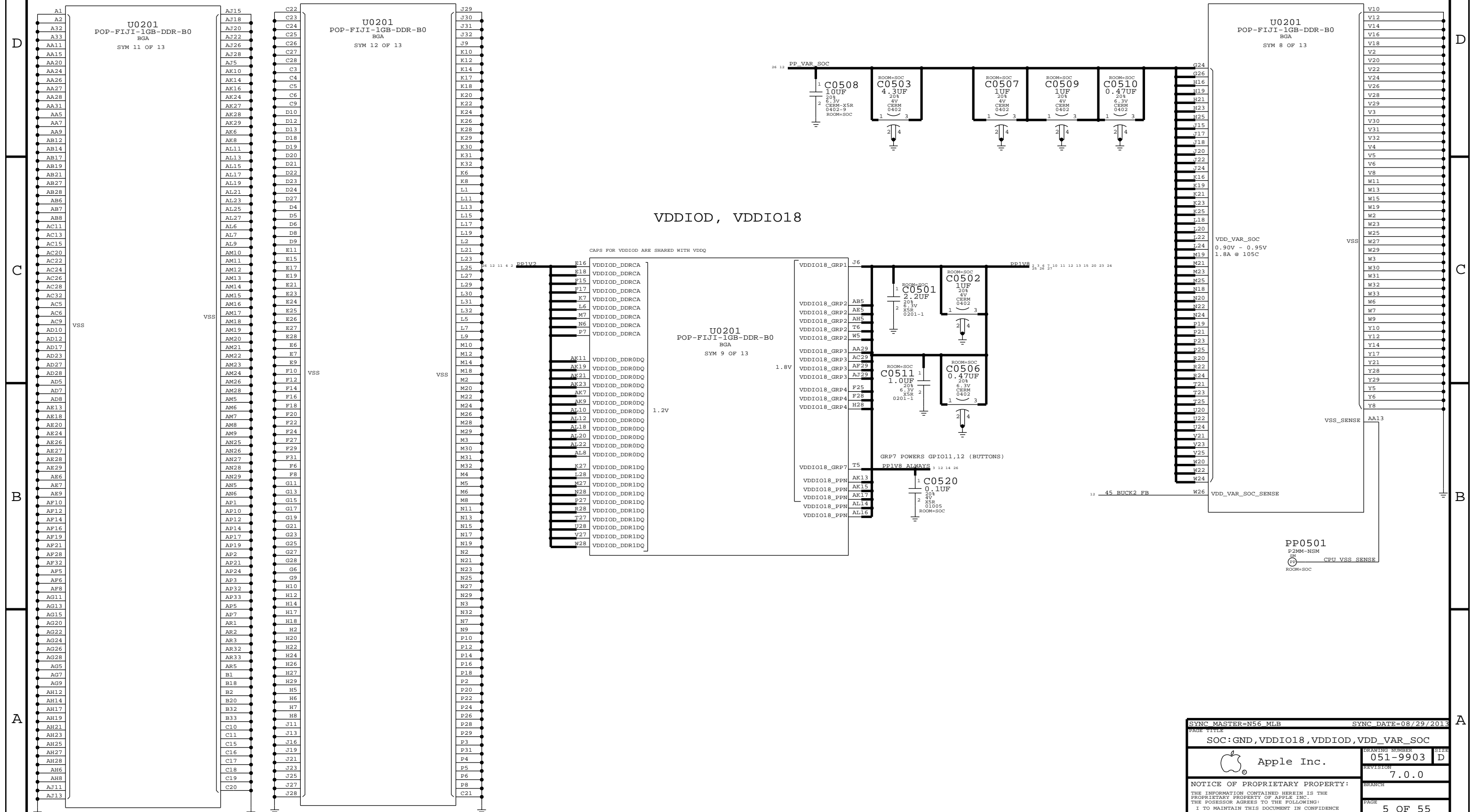


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PAGE TITLE		SOC:VDDCA,VDD1/2,VDD,VDD_CPU,VDD_GPU	
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FIJI: VDDIOD, VDDIO18, VDD_VAR_SOC

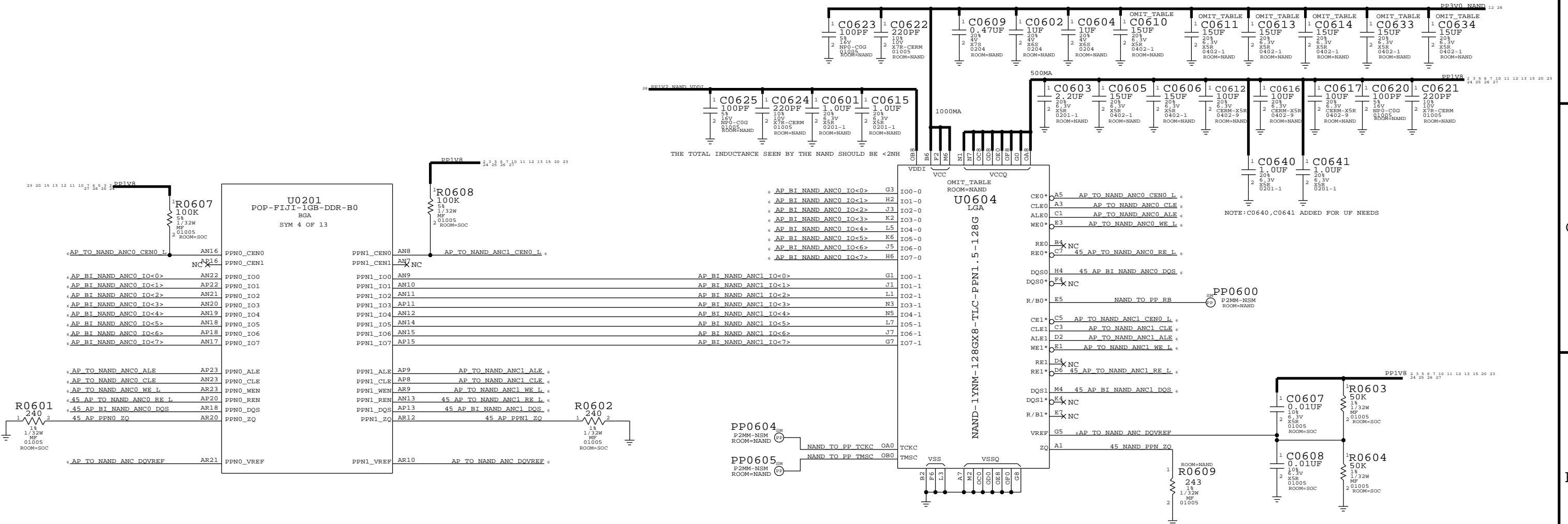
JUST A FEW GNDS

VDD_SRAM, VDD_SOC




FIJI: NAND + 12X17 NAND PKG

SUPPORT FOR PPN1.5 (1.8V IO) ONLY



NOTE: NAND PADS SHOULD BE SHIELDED FROM TRACES WITH A GROUND PLANE

PP0601 P4MM
ROOM=SOC 1 AP BI NAND ANCO IO<6> (IS A STATUS READY BIT)
PP0602 P4MM
ROOM=SOC 1 45 AP TO NAND ANCO RE L
PP0603 P4MM
ROOM=SOC 1 45 AP BI NAND ANCO DQS

SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
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SOC : NAND			
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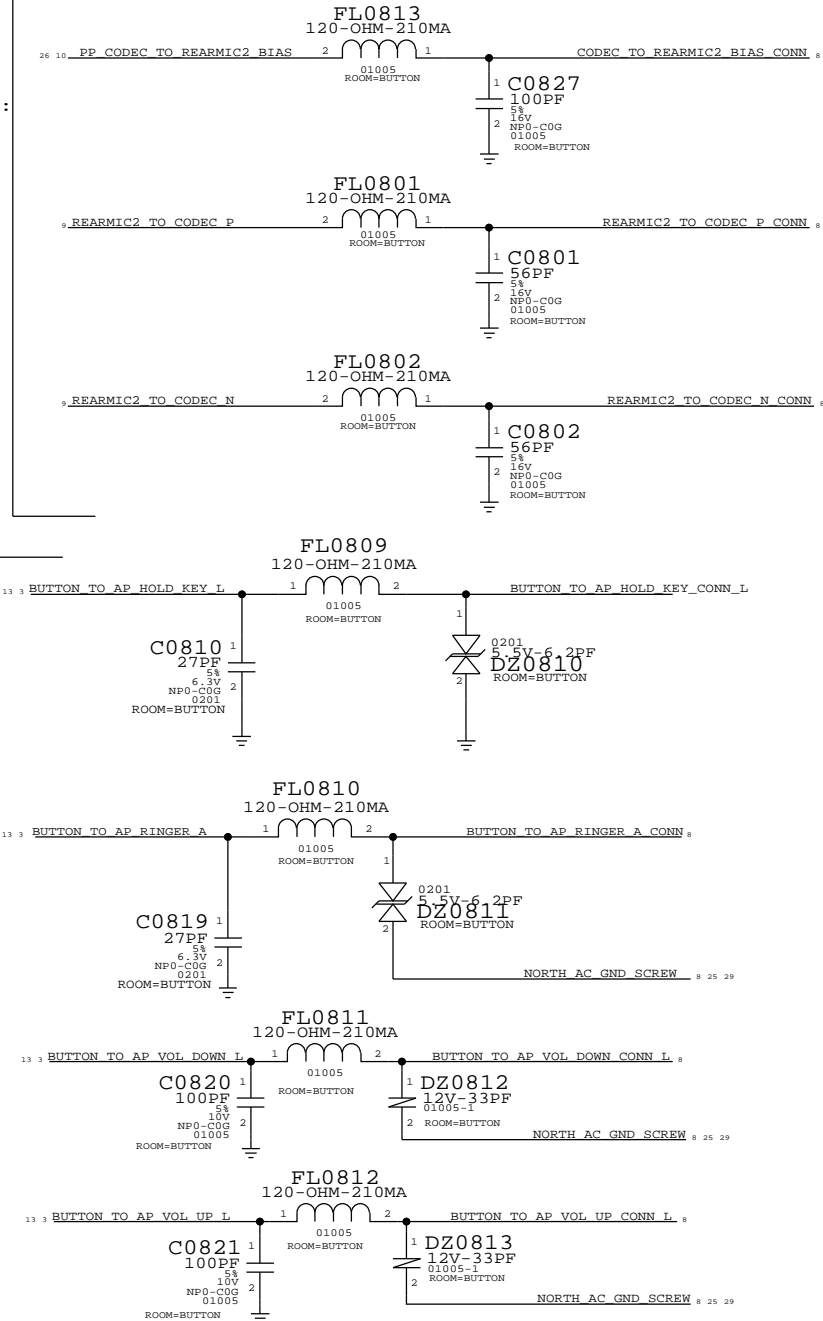
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8 7 6 5 4 3 2 1

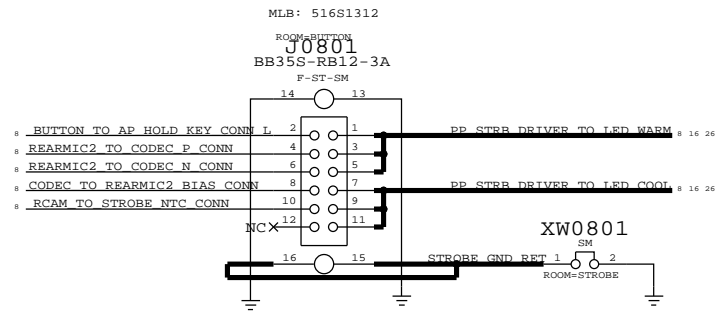
BUTTON FLEX (BUTTONS, ANC REF MIC, STROBE, STROBE_NTC, WIFI FLEX PAC)

MIC2 (ANC REF MIC):
MIC2/4 BIAS,
MIC2_P,_N

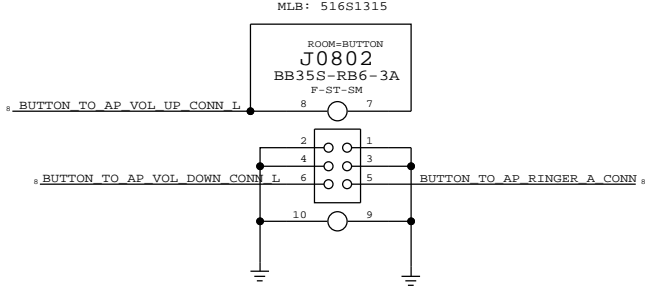
BUTTONS:
RINGER, HOLD,
VOL_UP/DOWN,



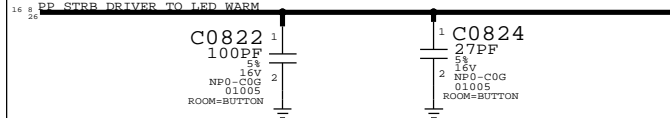
RIGHT BUTTON B2B



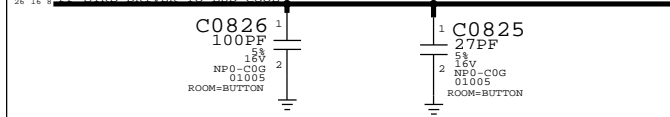
LEFT BUTTON B2B



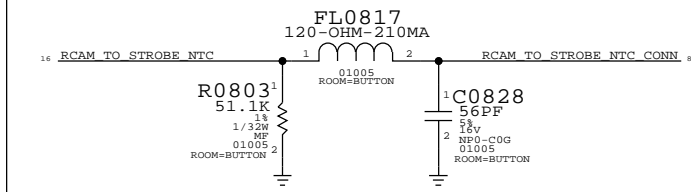
STROBE:
LED WARM




STROBE:
LED COOL



STROBE:
NTC

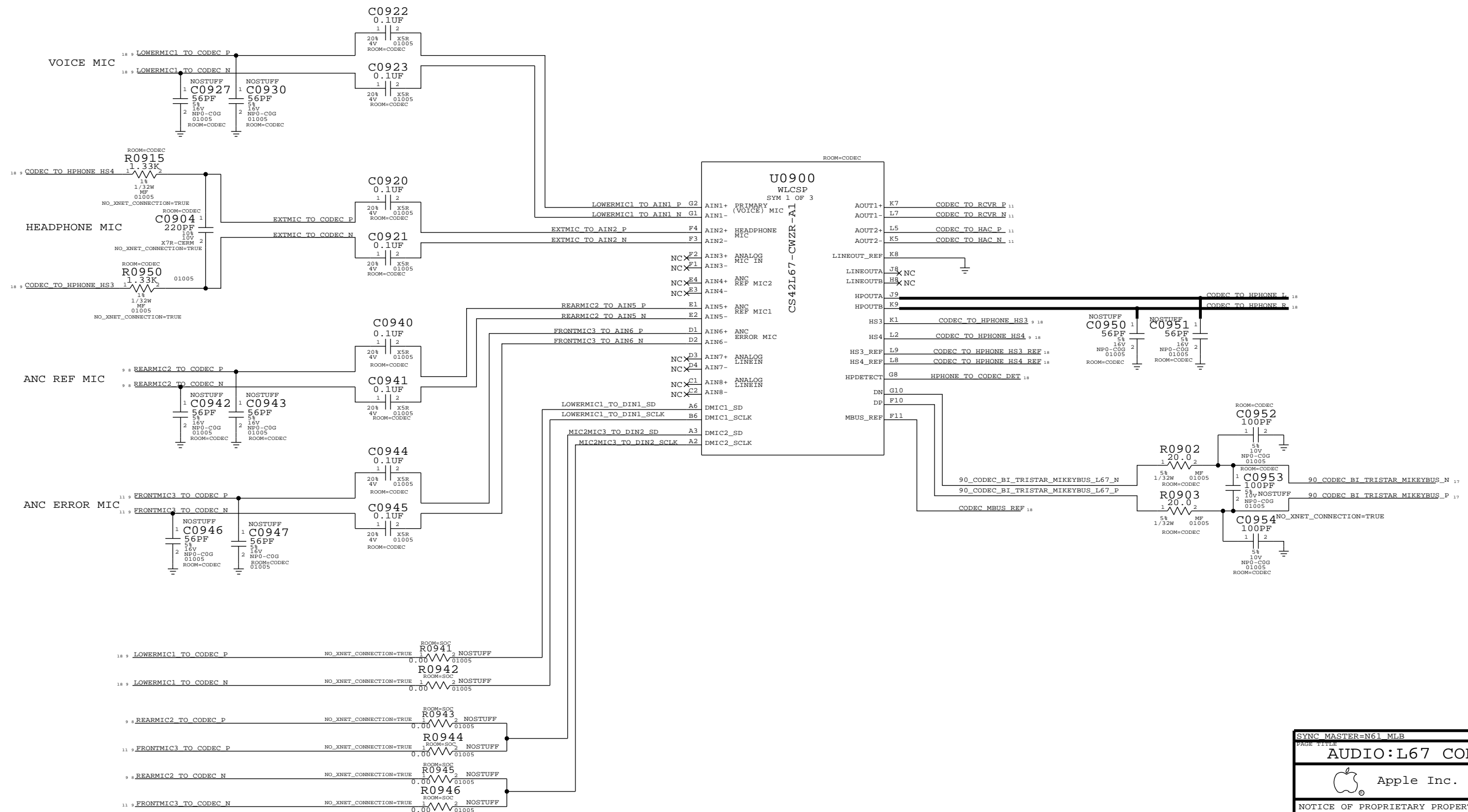



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
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IO:BUTTON FLEX CONN			
 Apple Inc.		DRAWING NUMBER	051-9903
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		BRANCH	
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L67 AUDIO CODEC

AUDIO I/O

(ANALOG MIC IN, DIG MIC IN, HPOUT, LINEOUT, RECEIVER OUT, MIKEYBUS)



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
AUDIO:L67 CODEC (1/2)			
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C

C

B

A

FRONT CAM FLEX B2B

(FCAM, PROX, ALS, RECEIVER, ANC ERROR MIC)

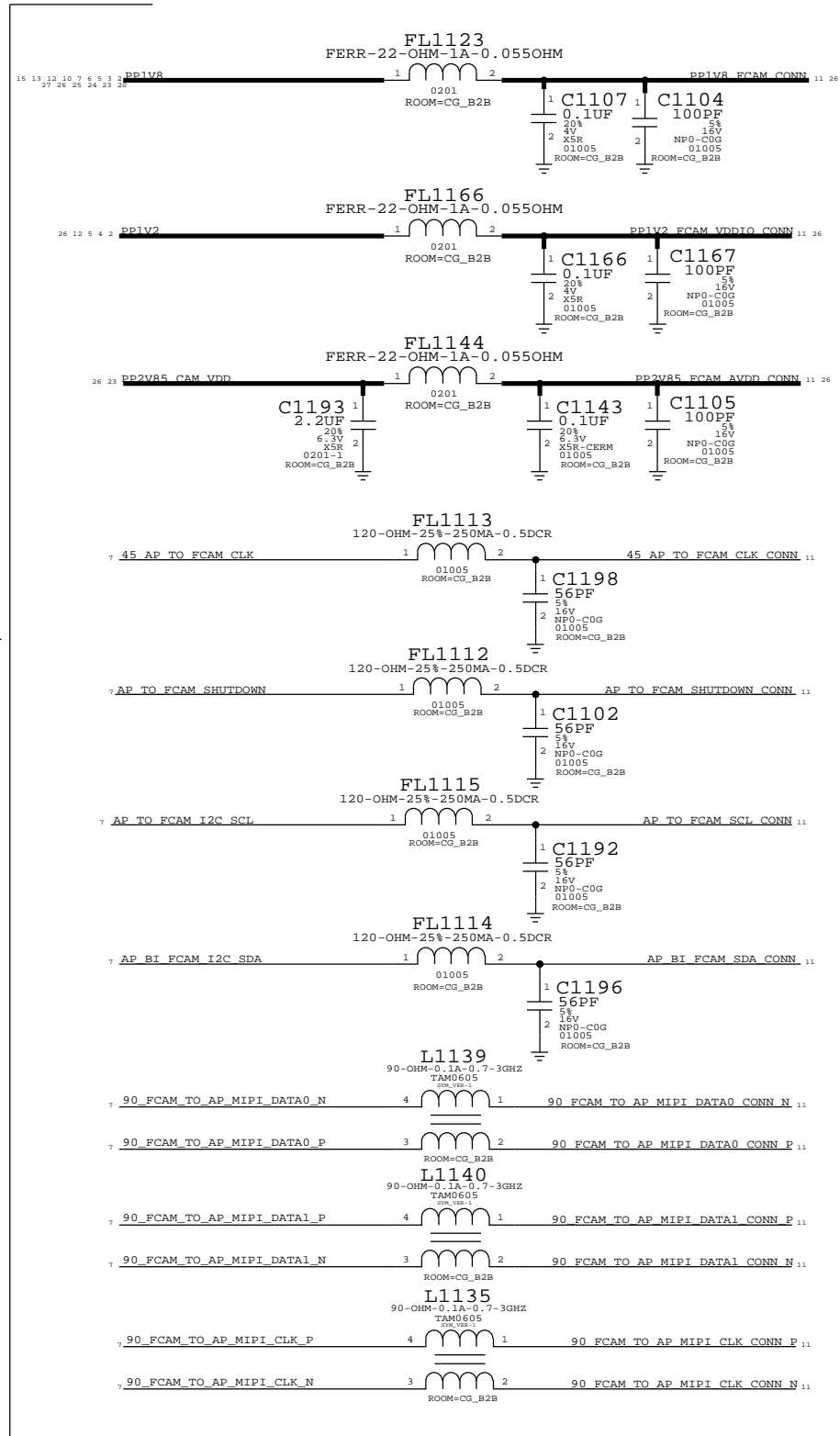
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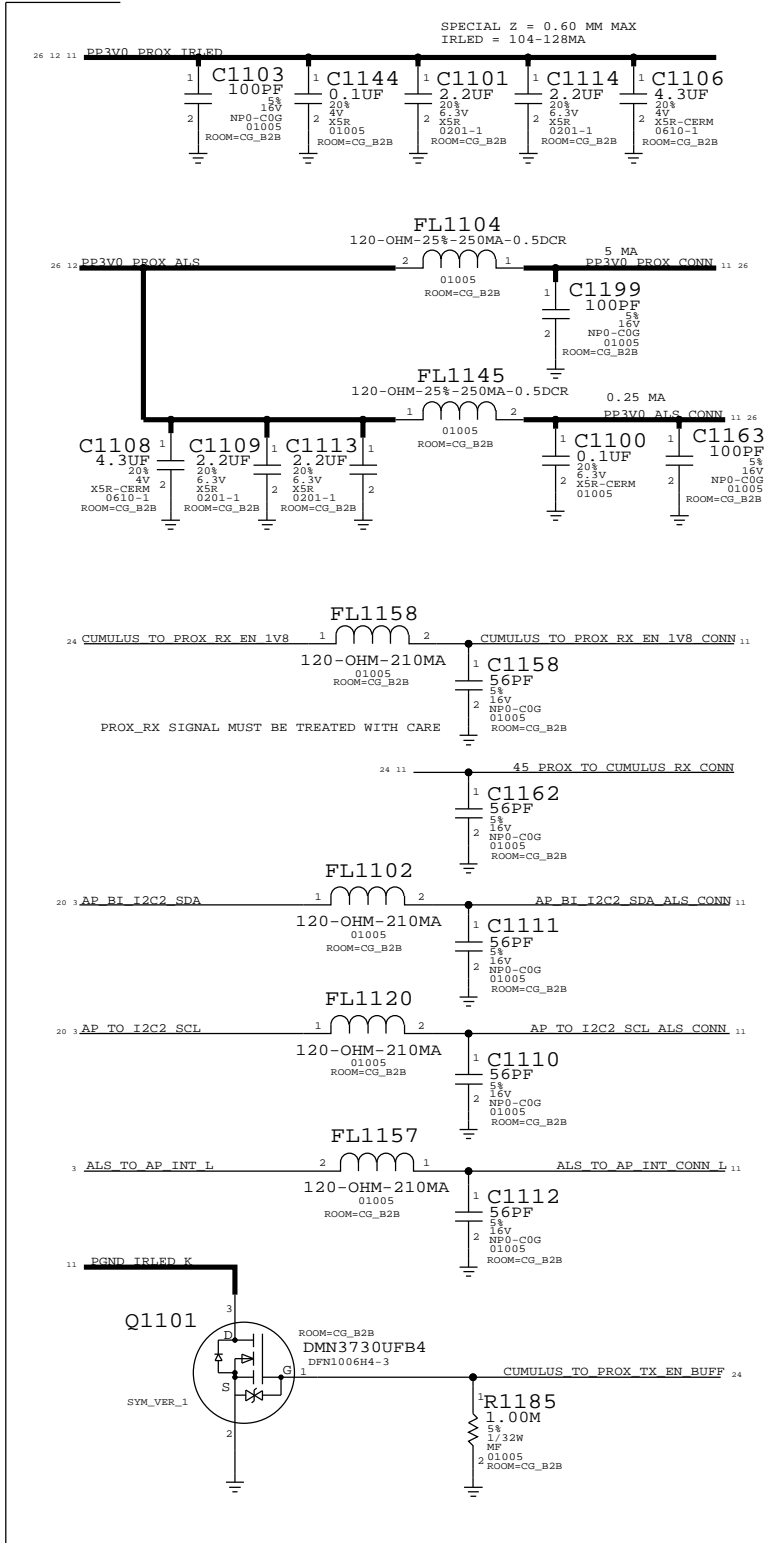
CAMERA

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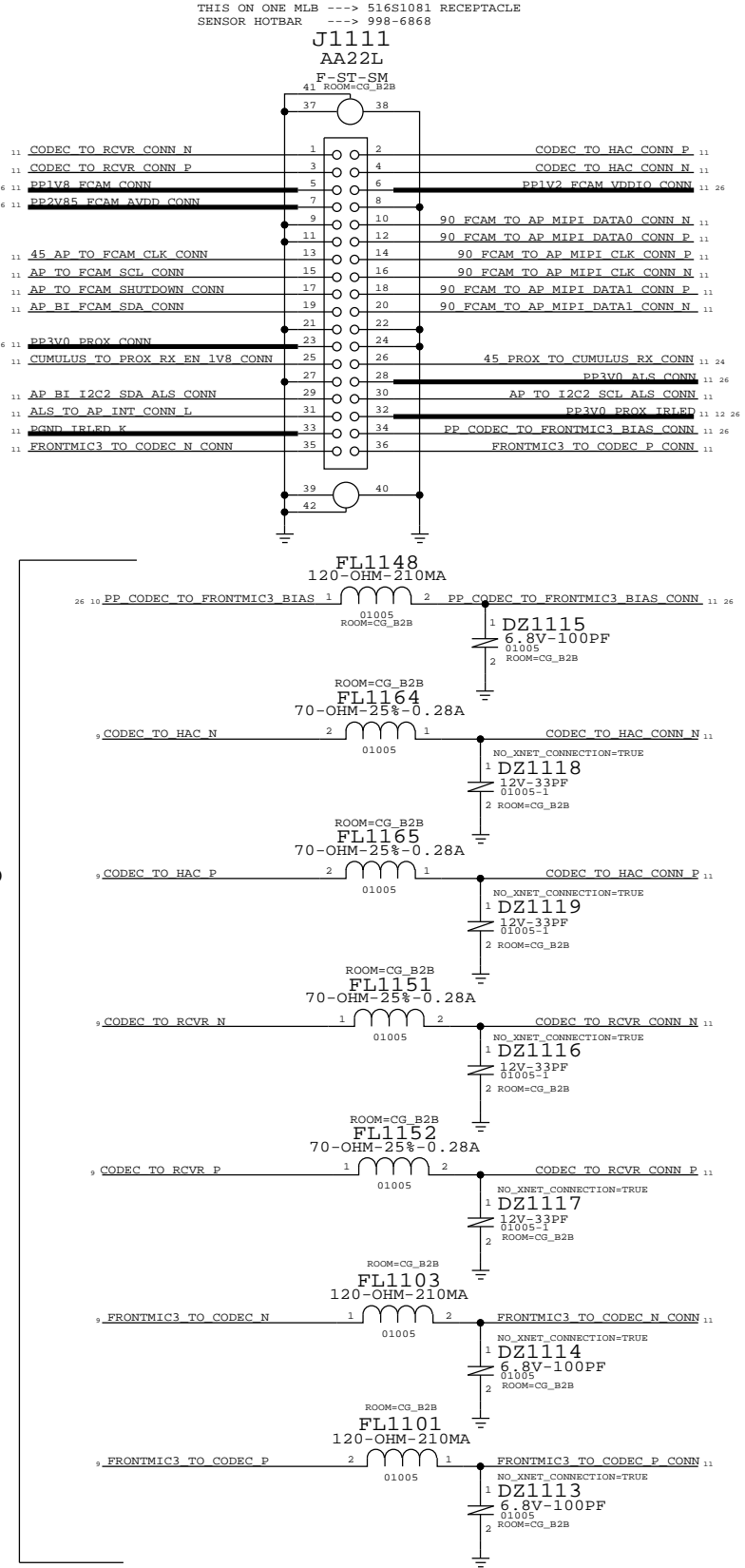
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ALS,
PROX



AUDIO

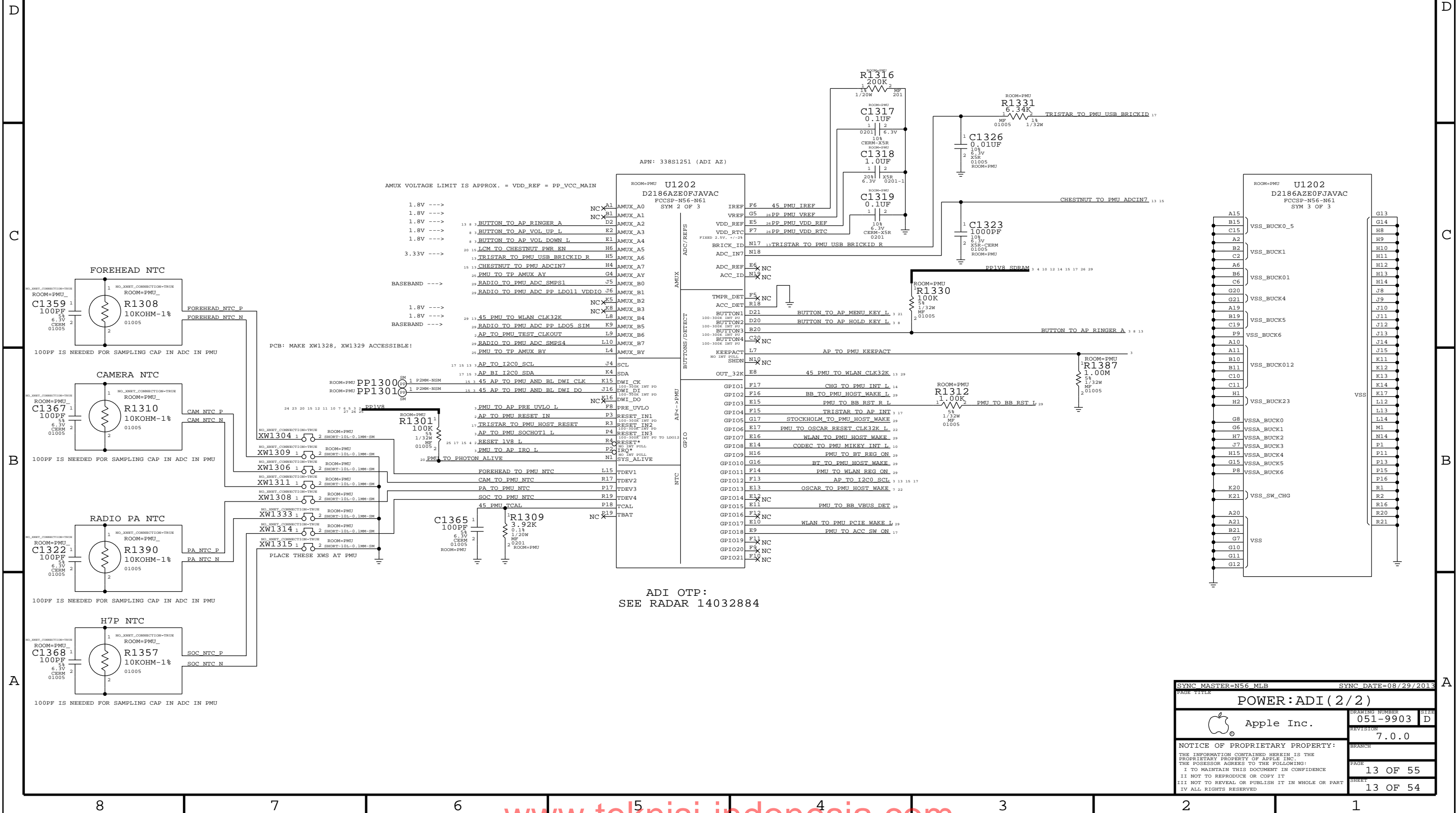


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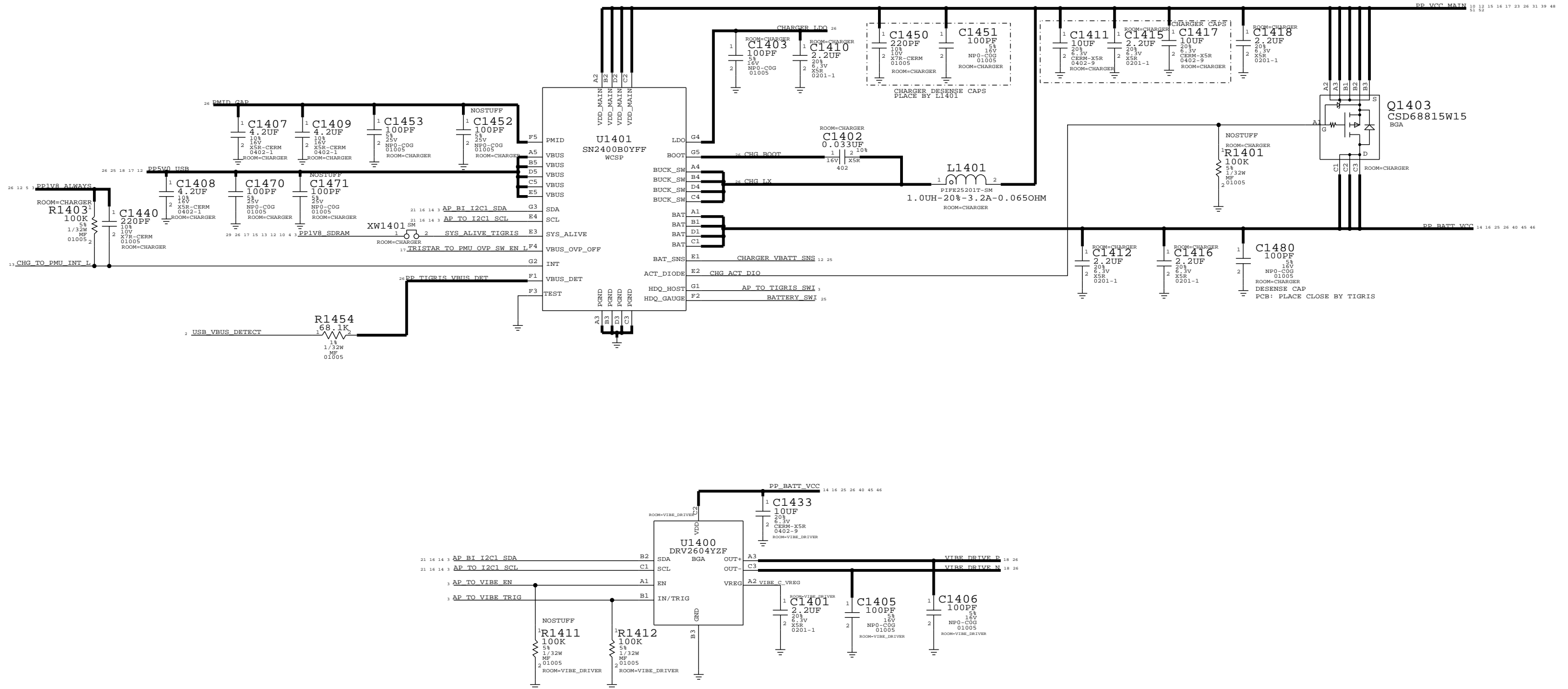
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```
(AMUX, GPIO, BUTTONS, ADC, THERMISTORS, SYSTEM I/F, GND)
```



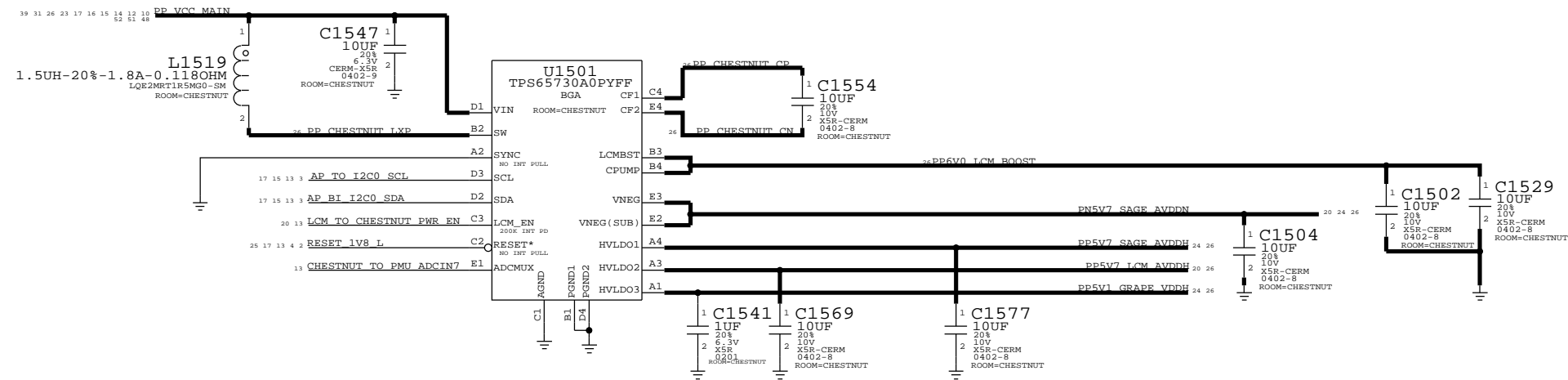
TIGRIS CHARGER & VIBE DRIVER



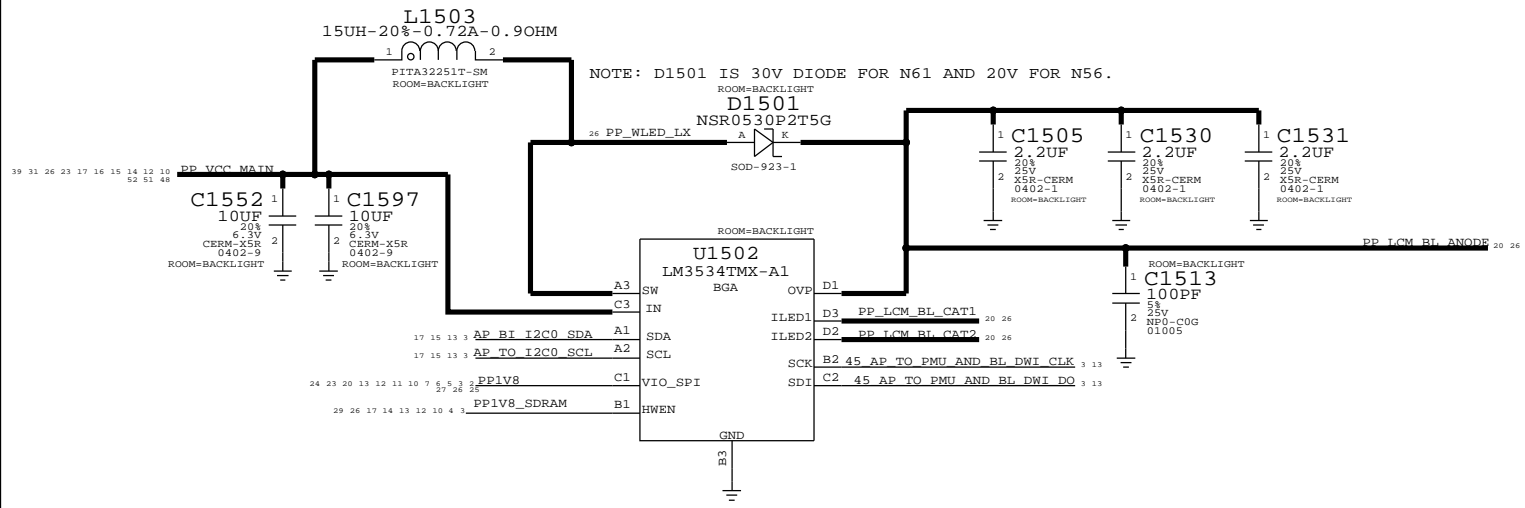
PAGE TITLE		
POWER:TIGRISR,VIBE DRIVER		
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CHESTNUT, BACKLIGHT DRIVER, MESA BOOST

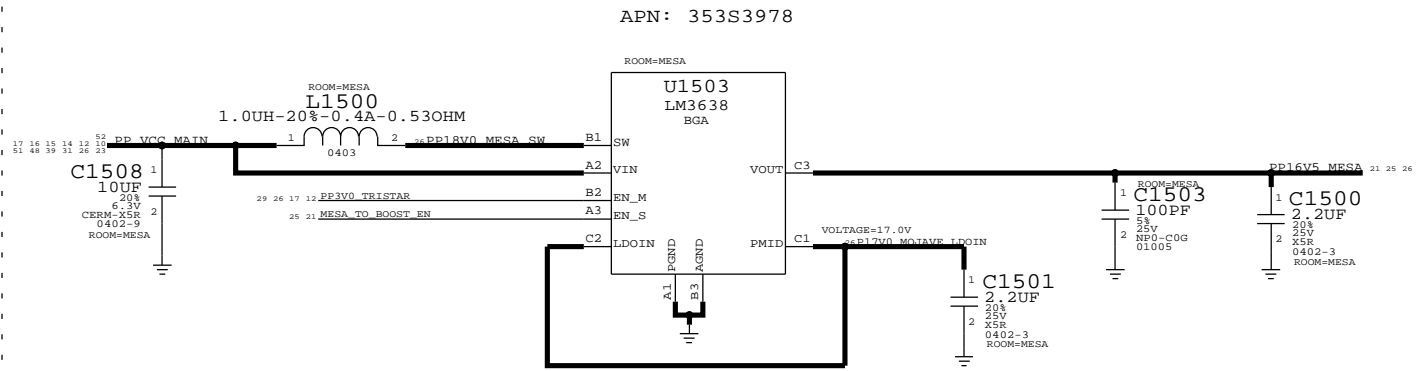
D500 DISPLAY PMU (TI CHESTNUT, 338S1149)



D500 BACKLIGHT DRIVER



MESA BOOST A0



PAGE TITLE		PAGE NUMBER	
DISPLAY:CHESTNUT, BACKLIGHT DRIVER		051-9903	
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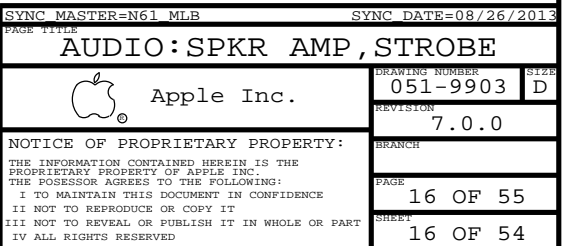
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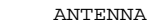
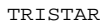
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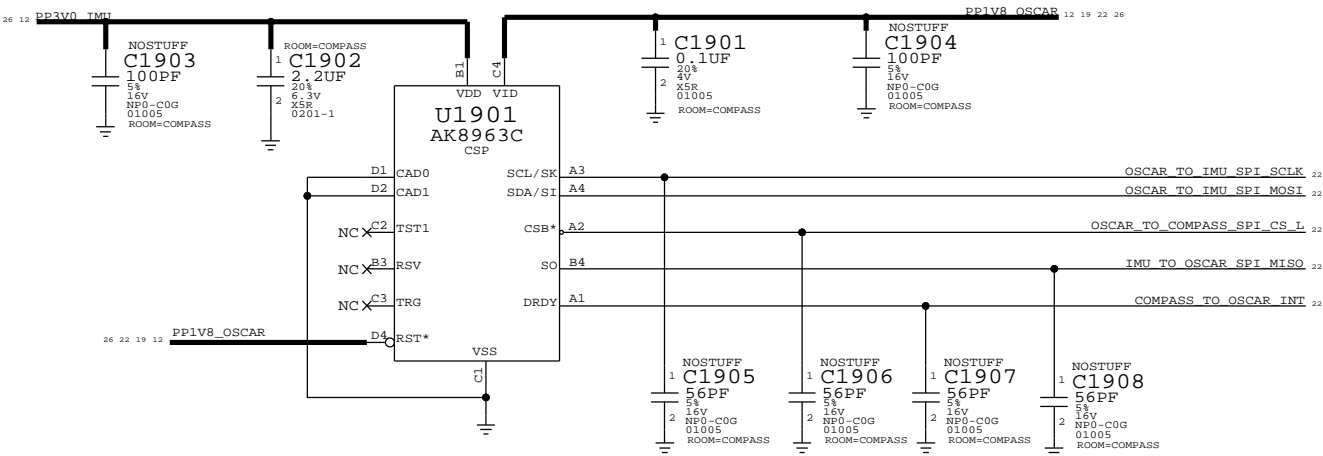
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


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BRANCH		PAGE	
		18 OF 55	
SHEET		18 OF 54	

COMPASS - AKM COMPASS IN POR LOCATION

COMPASS CSP: 338S1014

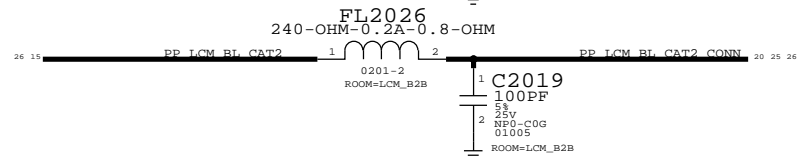
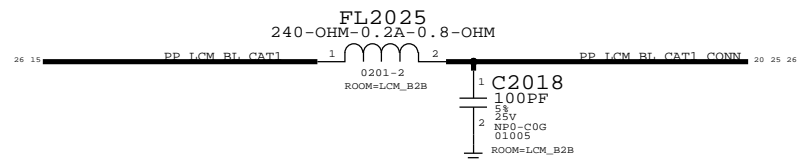
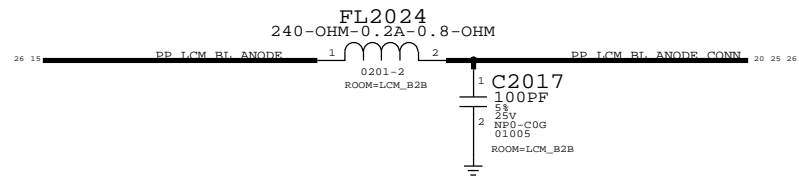


SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
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SENSORS : COMPASS			
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		REVISION	7.0.0
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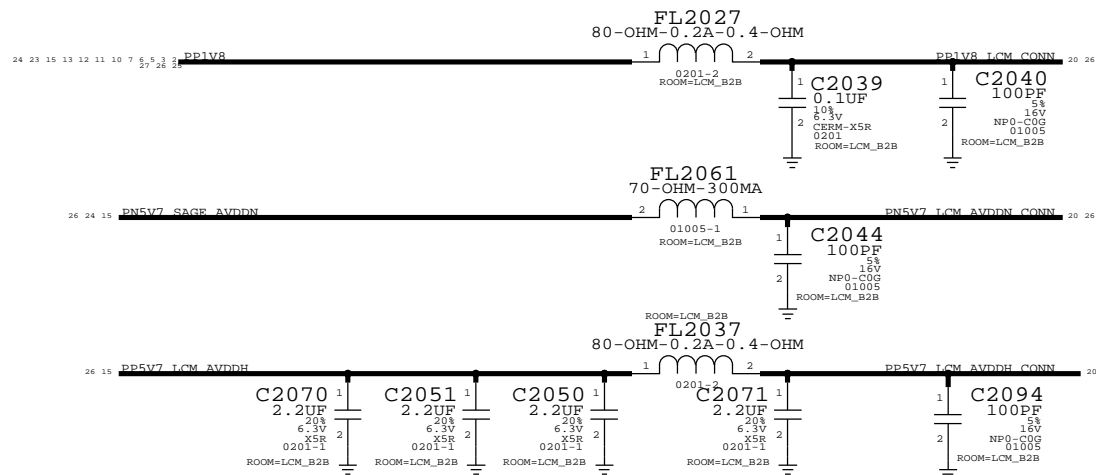
LCD B2B

Backlight

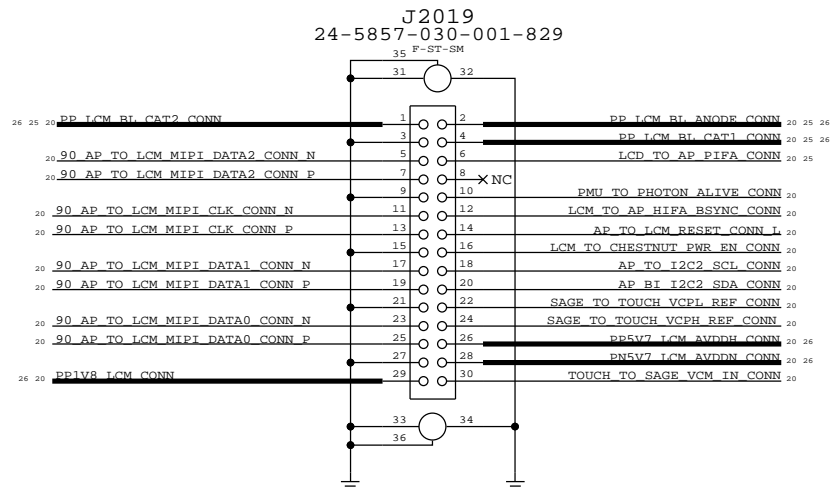
(N56 HAS A 2ND SET OF BL SIGNALS ON P. 19).



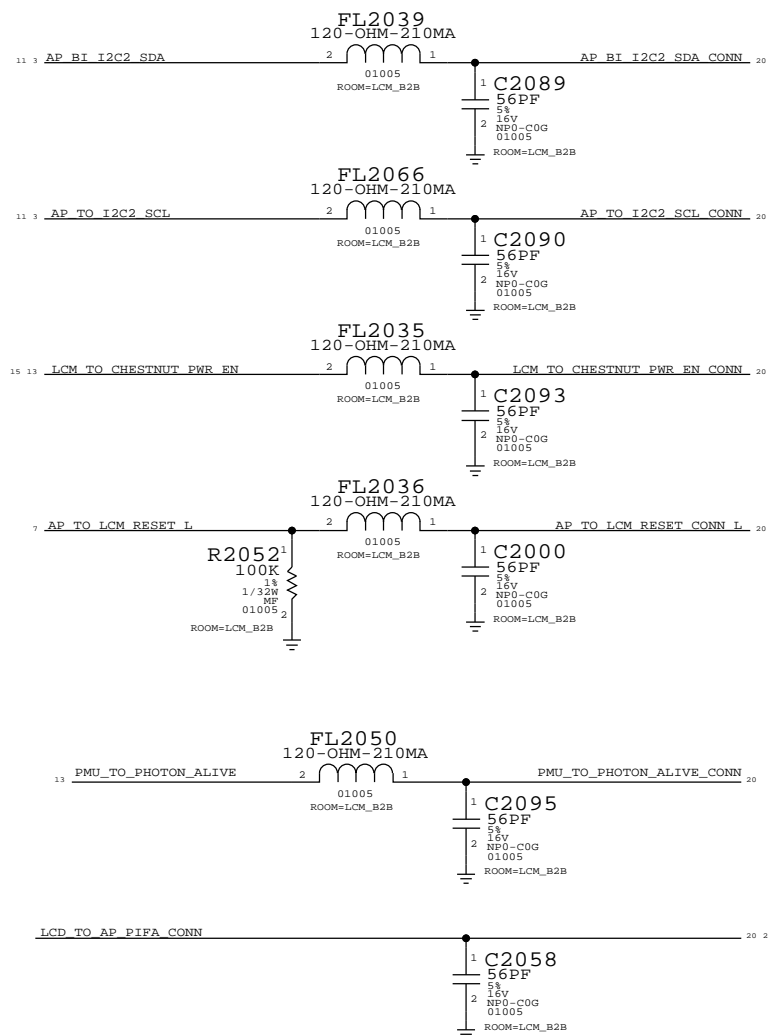
LCM Supplies



THIS ONE ON MLB ---> 516S1164

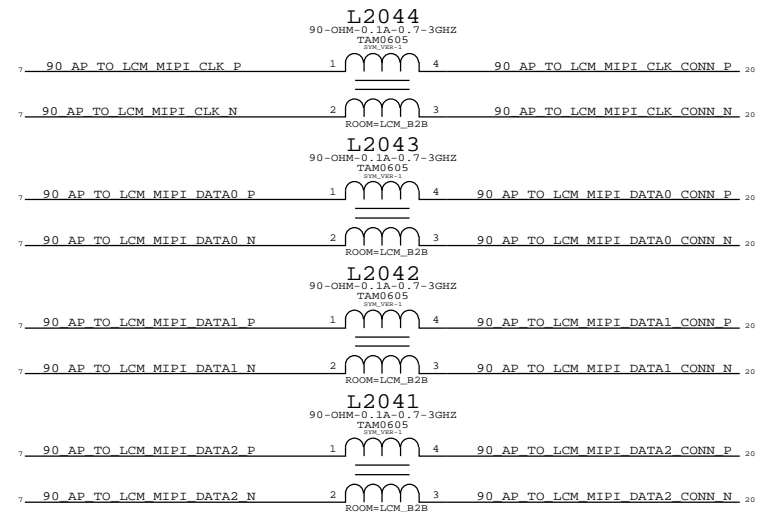


Digital Interfaces

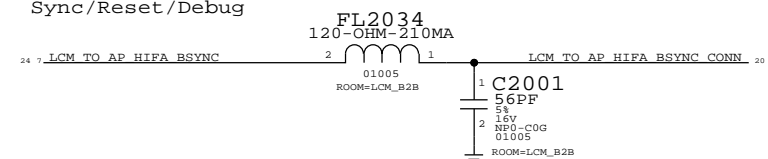


MIPI Common Mode Chokes

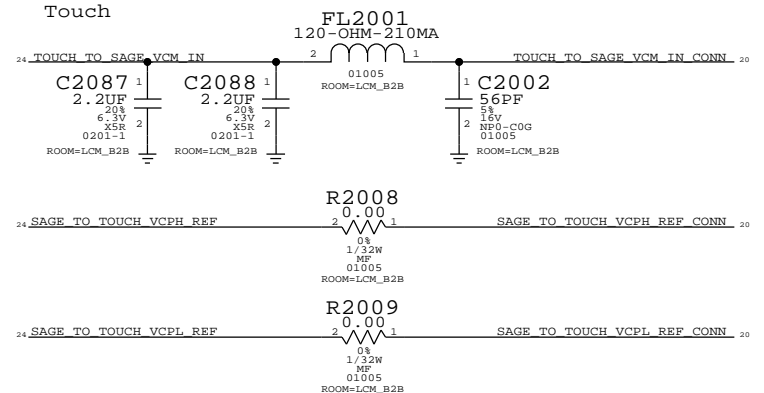
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Sync/Reset/Debug

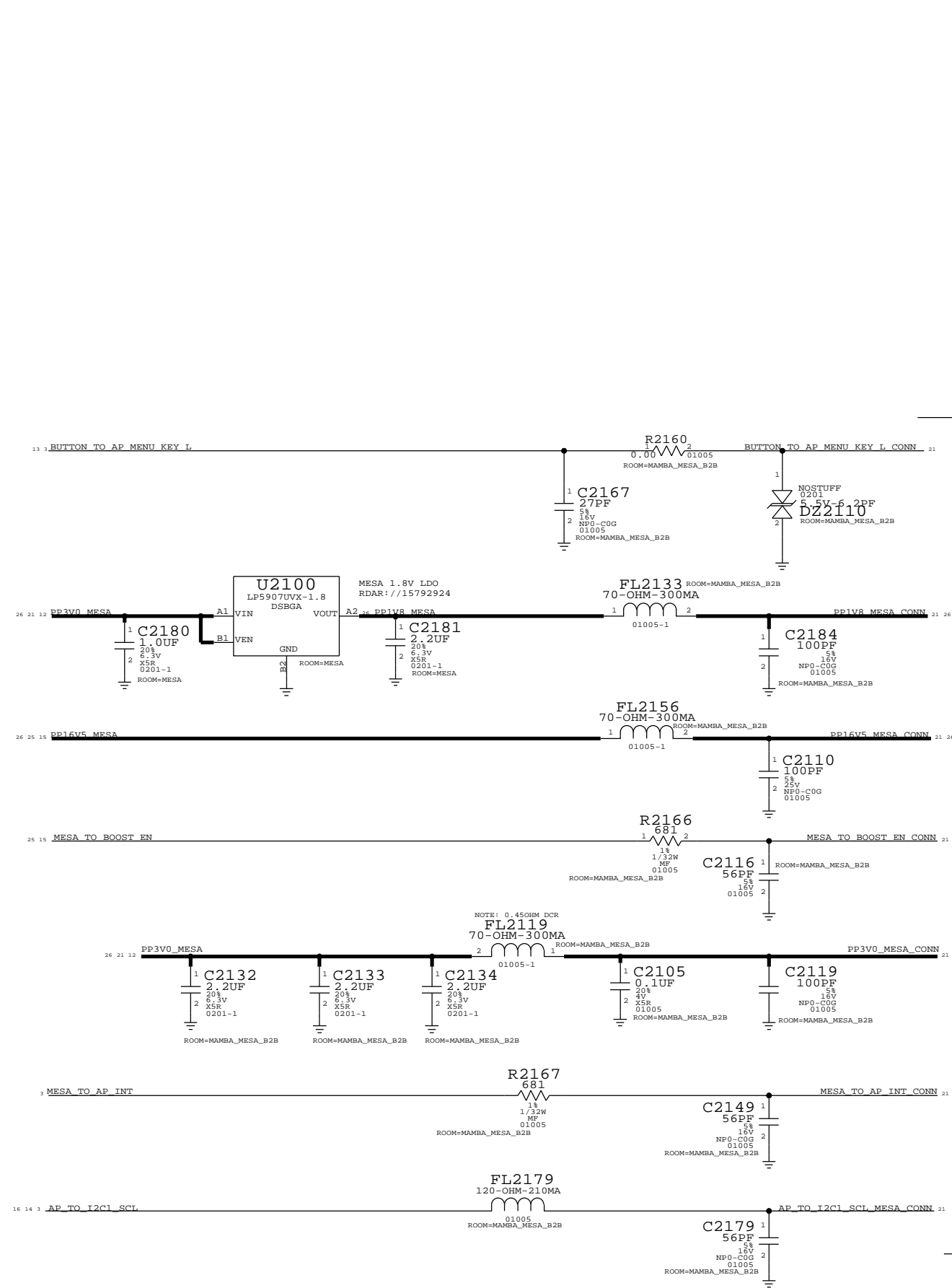


Touch

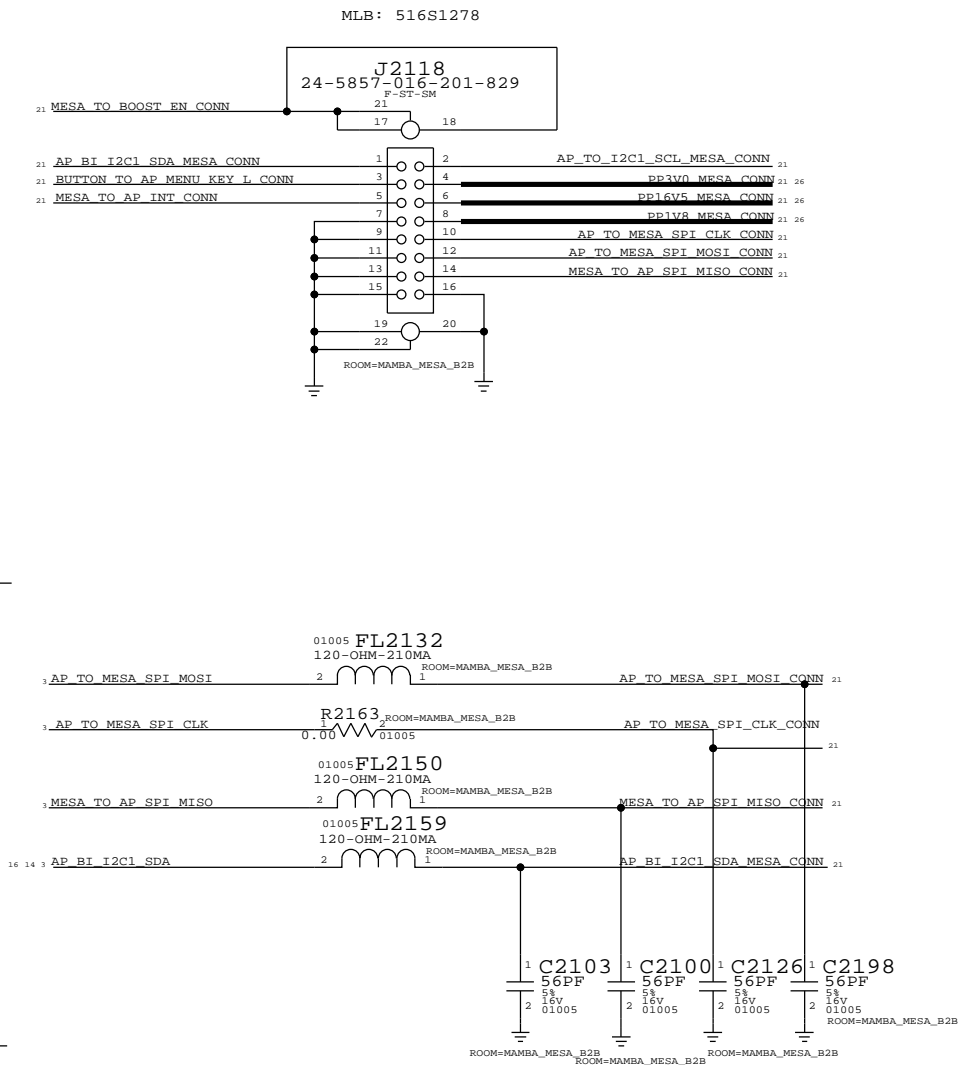



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DISPLAY:FLEX CONN		DRAWING NUMBER	051-9903
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MESA CONNECTOR



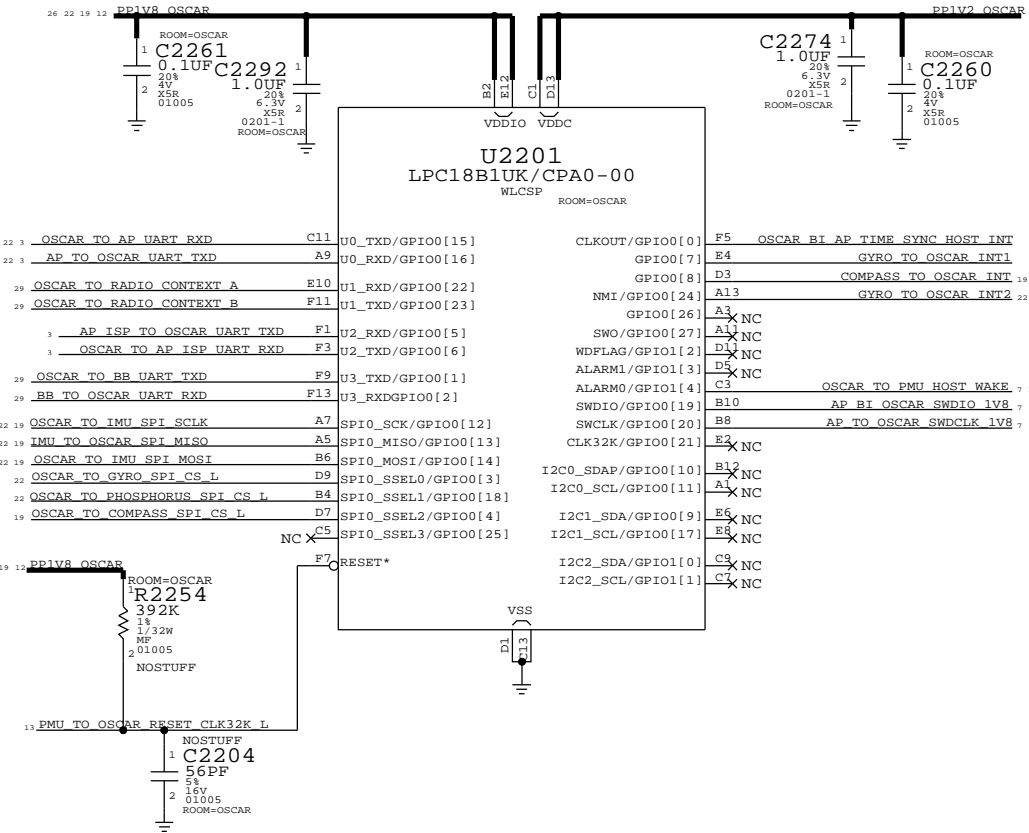
MESA SENSOR:



PAGE TITLE		
SENSORS:MESA FLEX CONN		
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	SHEET	21 OF 54

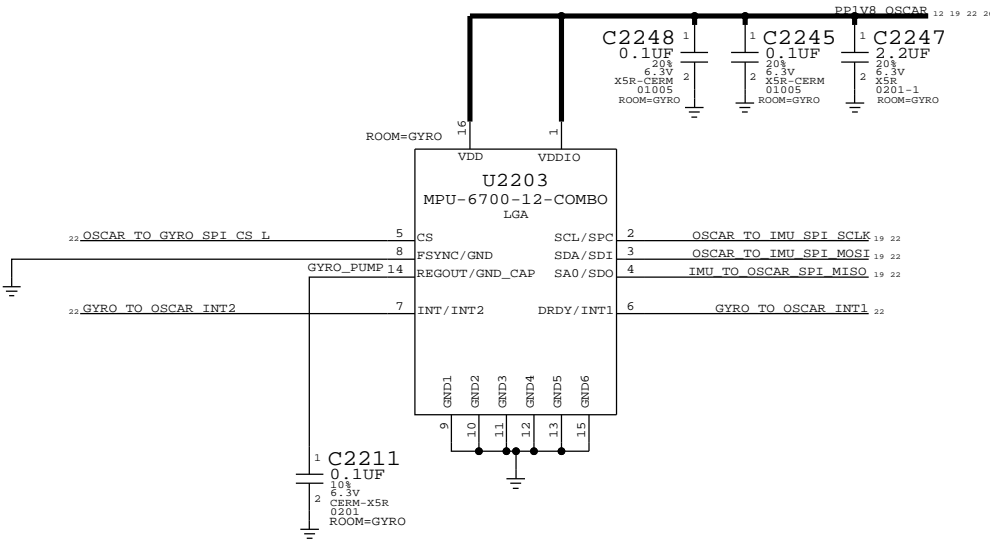
OSCAR + SENSORS

OSCAR VDDIO = 1.8V ALWAYS ON (NEED TO MAKE HOST & RUN PLL)
OSCAR CORE = 1.2V ALWAYS ON (NEED TO RUN IN SDRAM)



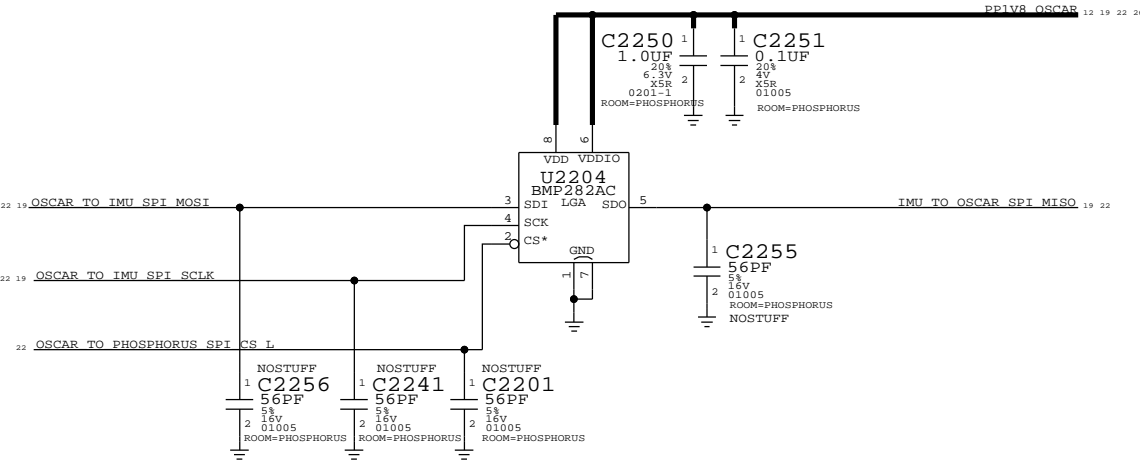
CARBON (ACCEL GYRO COMBO)


INVENSENSE, APN 338S00017, C2211=0.1UF
BOSCH, APN 338S00028, C2211=0.1UF
ST, APN 338S00029, C2211=0.01UF,25V



THIS IS OUTSIDE OF SHIELD IN
TO THE RIGHT OF THE NAND

PHOSPHORUS



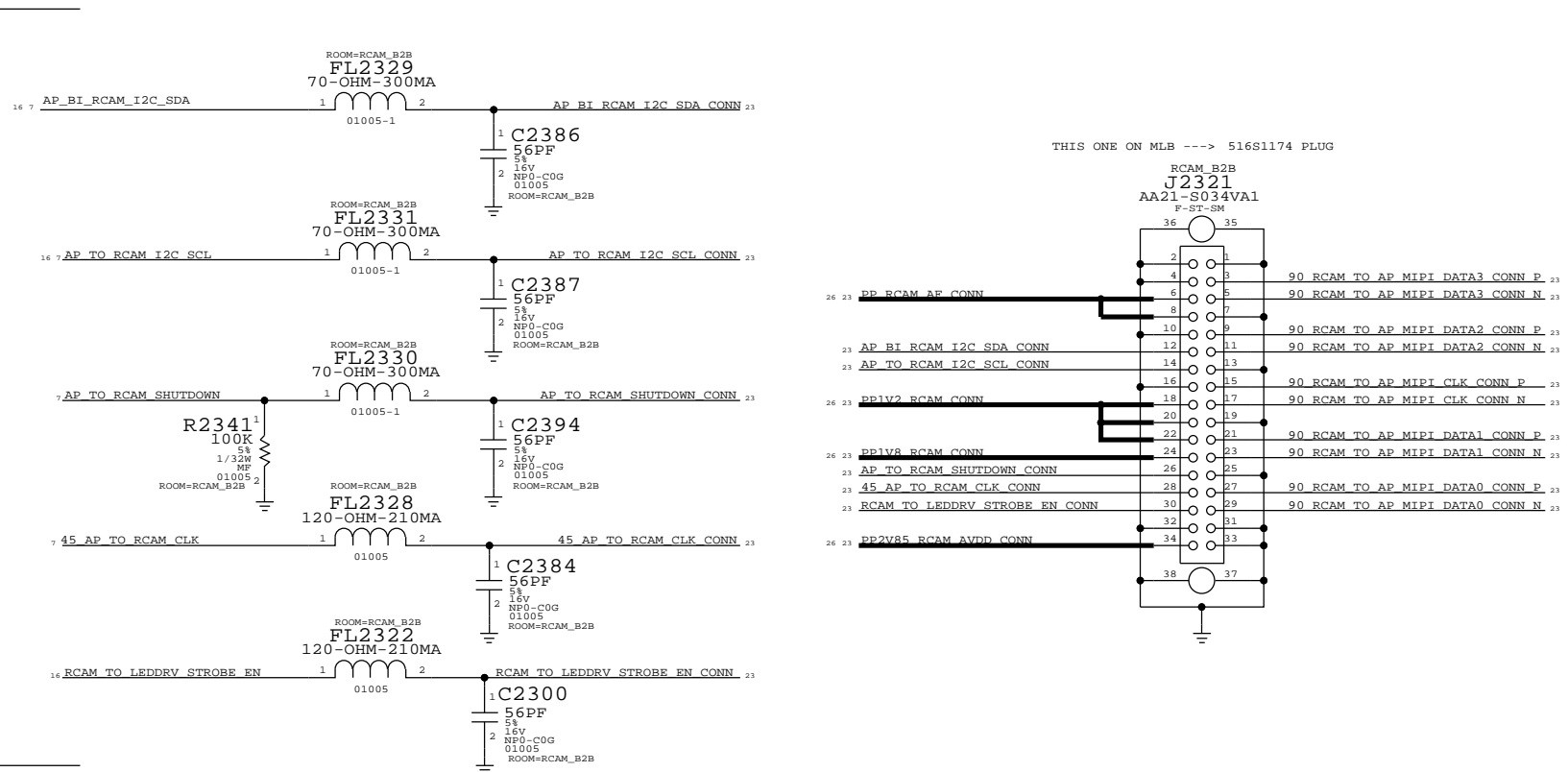
SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
SENSORS : OSCAR, CARBON, PHOS, MAGNESIUM			
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		PAGE	22 OF 55
		SHEET	22 OF 54

RCAM B2B (REAR CAMERA CONNECTOR)

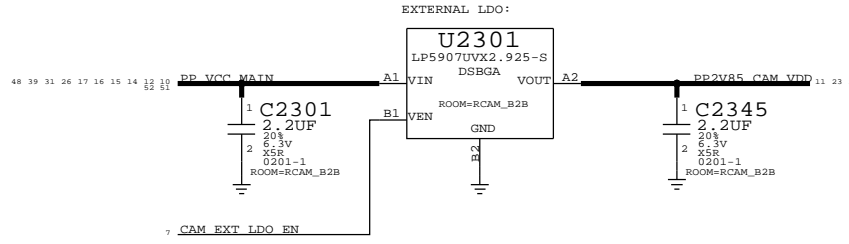
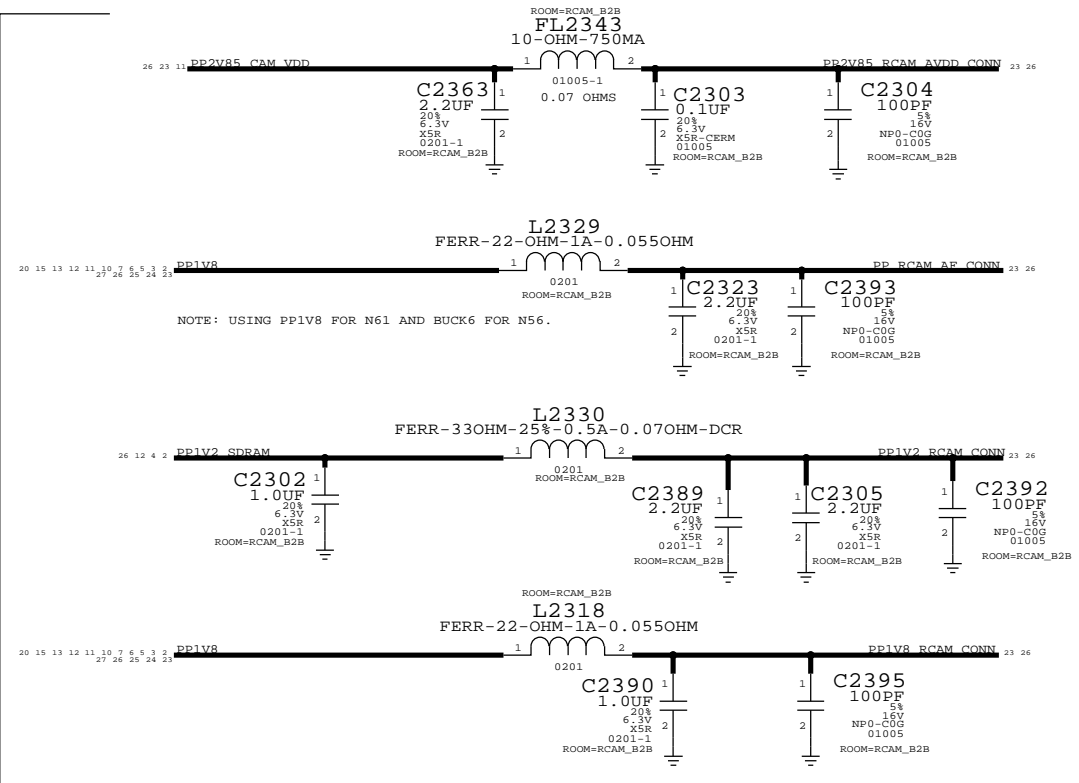
RCAM:
4-LANE MIPI


RCAM:
DIGITAL I/F
(I2C, CTRL, CLK)

RCAM:
POWER:
(1.8V DVDD)
(2.8V AVDD)
(1.2V VCC)
(1.8V/2V AF)



RCAM/FCAM AVDD RAIL EXT. LDO:



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
CAMERA:REAR FLEX CONN			
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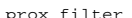
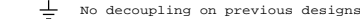
D



R

A

APN: 343S0638



I2C pull-ups

[illegible][illegible]


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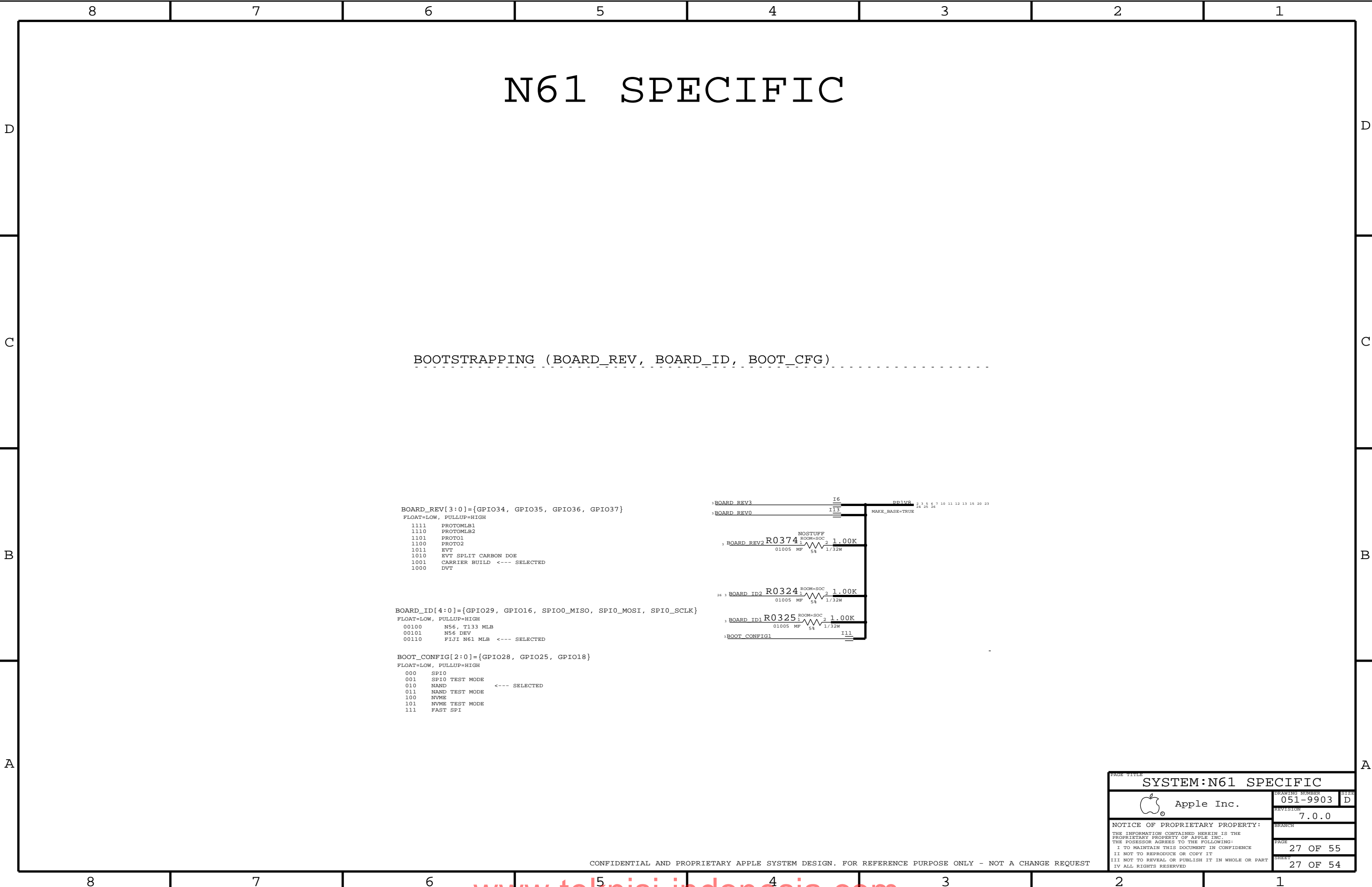


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VOLTAGE PROPERTIES

PP3V3 USB	3 12	PP LED DRV LX	14
VOLTAGE=1.8V	PP1V8 VA I19 I67	10 12 16	
VOLTAGE=3.0V	PP3V0 TRISTAR	12 15 17 29	
VOLTAGE=3.0V	PP3V0 IMU	12 19	
VOLTAGE=3.0V	PP3V0 NAND	6 12	
VOLTAGE=3.0V	PP3V3 ACC	12 17	
VOLTAGE=3.0V	PP3V0 PROX ALS	11 12	
VOLTAGE=4.6V	PP VCC MAIN	10 12 14 15 16 17 23 31 39	
VOLTAGE=1.0V	PP1V0	48 51 52	
VOLTAGE=3.0V	PP3V0 PROX TRIED	11 12	
VOLTAGE=1.8V	PP1V8 ALWAYS	1 5 12 14	
VOLTAGE=3.0V	PP3V0 MESA	12 21	
VOLTAGE=1.1V	PP CPU	4 12	
VOLTAGE=1.1V	PP GPU	4 12	
VOLTAGE=1.2V	PP1V2 SDRAM	2 4 12 23	
VOLTAGE=1.8V	PP1V8 SDRAM	3 4 10 12 13 14 15 17 29	
VOLTAGE=1.8V	PP1V8	2 3 4 5 7 10 11 12 13 15 20 23	
VOLTAGE=1.8V	PP1V8 GRAPE	12 24	
VOLTAGE=1.8V	PP1V8 OSCAR	12 19 22	
VOLTAGE=1.2V	PP1V2 NAND VDDT	6	
VOLTAGE=1.8V	PP EXTMIC BIAS FILT IN	10	
VOLTAGE=1.8V	BOARD_ID2	3 27	
VOLTAGE=1.2V	PP1V2	2 4 5 11 12	
VOLTAGE=5.0V	PP_E75_TO_TRISTAR_ACC1_CONN	18 25	
VOLTAGE=5.0V	PP_E75_TO_TRISTAR_ACC1	17 18	
VOLTAGE=22.0V	PP_LCM_BL_ANODE_CONN	15 20	
VOLTAGE=0.2V	PP_LCM_BL_CAT2	15 20	
VOLTAGE=0.2V	PP_LCM_BL_CAT1	15 20	
VOLTAGE=0.2V	PP_LCM_BL_CAT2_CONN	20 25	
VOLTAGE=0.2V	PP_LCM_BL_CAT1_CONN	20 25	
VOLTAGE=-5.7V	PP5V7 SAGE AVDDN	15 20 24	
VOLTAGE=1.2V	PP1V2 OSCAR	12 22	
VOLTAGE=3.0V	PP3V0 MESA_CONN	21	
VOLTAGE=6V	PP6V0 LCM BOOST	16	
VOLTAGE=5.0V	PP_STRB_DRIVER_TO_LED_WARM	8 16	
VOLTAGE=5.0V	PP_STRB_DRIVER_TO_LED_COOL	8 16	
VOLTAGE=1.8V	PP CODEC TO MIC1 BIAS	10 18	
VOLTAGE=1.8V	PP EXTMIC BIAS IN	10	
VOLTAGE=1.8V	PP EXTMIC BIAS FILT	10	
VOLTAGE=1.8V	PP_CODEC_TO_FRONTMIC3_BIAS	10 11	
VOLTAGE=1.8V	PP_CODEC_TO_REARMIC2_BIAS	8 10	
VOLTAGE=1.8V	PP CODEC FILT+	10	
VOLTAGE=2.2V	PP CODEC SPKR VO	10	
VOLTAGE=2.5V	PP CODEC VCPH1LT	10	
VOLTAGE=2.5V	PP CODEC VCPH1LT+	10	
VOLTAGE=2.5V	PP CODEC VHP ELVN	10	
VOLTAGE=0.2V	PP CODEC VHP ELVX	10	
VOLTAGE=2.5V	PP CODEC VHP ELVY	10	
VOLTAGE=1.8V	PP1V8 RCAM_CONN	11	
VOLTAGE=3.0V	PP2V85 RCAM AVDD_CONN	11	
VOLTAGE=1.8V	PP_CODEC_TO_FRONTMIC3_BIAS_CONN	11	
VOLTAGE=3.0V	PP3V0 ALS_CONN	11	
VOLTAGE=1.2V	PP1V2 RCAM VDDIO_CONN	11	
VOLTAGE=5.0V	PP5V0 USB	12 14 17 18 25	
VOLTAGE=5.0V	PP5V0 USB TO PMU	12	
VOLTAGE=4.6V	PP_BUCK5_LX	12	
VOLTAGE=4.6V	PP_BUCK3_LX	12	
VOLTAGE=4.6V	PP_BUCK4_LX	12	
VOLTAGE=4.6V	PP_BUCK2_LX	12	
VOLTAGE=4.6V	PP_BUCK1_LX1	12	
VOLTAGE=4.6V	PP_BUCK1_LX0	12	
VOLTAGE=4.6V	PP_BUCK0_LX3	12	
VOLTAGE=4.6V	PP_BUCK0_LX2	12	
VOLTAGE=4.6V	PP_BUCK0_LX1	12	
VOLTAGE=4.6V	PP_BUCK0_LX0	12	
VOLTAGE=6.0V	PP_CHESTNUT_LXP	15	
VOLTAGE=6.0V	PP_CHESTNUT_CP	15	
VOLTAGE=6.0V	PP_CHESTNUT_CN	15	
VOLTAGE=5.7V	PP5V7 SAGE AVDDH	16 24	
VOLTAGE=5.7V	PP5V7 LCM AVDDH	15 20	
VOLTAGE=5.1V	PP5V1 GRAPE VDDH	15 24	
VOLTAGE=22.0V	PP_WLED_LX	15	
VOLTAGE=18.0V	PP18V0 MESA_SW	15	
VOLTAGE=17.0V	PP17V0 MOWAVE I2OIN	15	
VOLTAGE=16.5V	PP16V5 MESA	15 21 25	
VOLTAGE=8.0V	PP_SPKAMP_SW	16	
VOLTAGE=8.0V	PP_I19_VBOOST	16	
VOLTAGE=1.8V	PP_SPKAMP_FILT	16	
VOLTAGE=1.8V	PP_SPKAMP_LDO_FILT	16	
VOLTAGE=5.0V	PP TRISTAR_PIN	17	
VOLTAGE=1.8V	PP1V8 LCM_CONN	20	
VOLTAGE=22.0V	PP_LCM_BL_ANODE_CONN	20 25	
VOLTAGE=-5.7V	PP5V7 LCM AVDDN_CONN	20	
VOLTAGE=5.7V	PP5V7 LCM AVDDH_CONN	20	
VOLTAGE=1.8V	PP1V8 MESA	21	
VOLTAGE=16.5V	PP16V5 MESA_CONN	21	
VOLTAGE=5.0V	PP TRISTAR_PIN	17	
VOLTAGE=1.2V	PP1V2 RCAM_CONN	23	
VOLTAGE=1.8V	PP1V8 RCAM_CONN	23	
VOLTAGE=3.0V	PP2V85 CAM_VDD	11 23	
VOLTAGE=1.8V	PP2V85 RCAM AVDD_CONN	23	
VOLTAGE=1.8V	PP_CUMULUS_VDDCORE	24	
VOLTAGE=1.2V	PP_CUMULUS_VDDANA	24	
VOLTAGE=13.5V	PP_SAGE_TO_TOUCH_VCPH_CONN	24	
VOLTAGE=-12V	PP_SAGE_TO_TOUCH_VCPH_CONN	24	
VOLTAGE=13.5V	PP_SAGE_TO_TOUCH_VCPH	24	
VOLTAGE=-12V	PP_SAGE_TO_TOUCH_VCPH	24	
VOLTAGE=-12V	PP_SAGE_VCPH_F	24	
VOLTAGE=5.7V	PP_SAGE_LX	24	
VOLTAGE=17.0V	PP_SAGE_LX	24	
VOLTAGE=1.8V	PP_PMI_VREF	13	
VOLTAGE=14V	PP_SAGE_VBST_OUTH	24	
VOLTAGE=5.0V	PP_TIGRIS_VBUS_DET	14	
VOLTAGE=2.5V	PP1V8_PLL PP_MIPIOD_VREG BOARD_ID0	10	
VOLTAGE=1.8V	PP_PMI_VDD_BPS	10	
VOLTAGE=1.8V	PP_EXTMIC_BTAS	10	
VOLTAGE=1.8V	PP1V8_XTAL	13	
VOLTAGE=1.8V	PP_PMI_VDD_RTC	13	
VOLTAGE=4.6V	PP_BATT_VCC	14 16 25 40 45 46	
VOLTAGE=1.8V	PP1V8 MESA_CONN	21	
VOLTAGE=3.0V	PP3V0 PROX_CONN	11	
VOLTAGE=1.0V	PP0V95_FIXED_SOC	4 7 12	
VOLTAGE=1.0V	PP0V95_FIXED_SOC_RCIE	7	
VOLTAGE=1.2V	PP1V2_PLL	2	
VOLTAGE=1.0V	PP_BUCK5_LX1	12	
VOLTAGE=1.0V	PP_VAR_SOC	5 12	
VOLTAGE=5.0V	PMID_CAP	14	
VOLTAGE=5.0V	CHARGER_LDO	14	
VOLTAGE=4.6V	CHG_BOOT	14	
VOLTAGE=4.6V	CHG_LX	14	
VOLTAGE=3.0V	VIBR_DRIVE_P	14 18	
VOLTAGE=3.0V	VIBR_DRIVE_N	14 18	
VOLTAGE=1.8V	PP_RCAM_AE_CONN	23	
VOLTAGE=-14.0V	PP_SAGE_VBST_OUTH	24	
VOLTAGE=-12.0V	PP_SAGE_TO_TOUCH_VCPH_FILT	24	
VOLTAGE=2.7V	PP_BB_VDD_2V7_CONN	18	

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C									C						
B									B						
A									A						
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RADIO_MLB HIERARCHICAL SYMBOL

POWER

VCC_MAIN, VBAT GOES TO RADIO_MLB DIRECTLY
CHECK ALL PAGES IN RF SIDE!

CELLULAR HOUSE KEEPING

3	AP_TO_RADIO_ON_L	MAKE_BASE+TRUE	I325	RADIO_ON_L	30 32
3	BB_TO_AP_RESET_DET_L	MAKE_BASE+TRUE	I324	BB_RESET_DET_L	30 35
13	PMU_TO_BB_RST_L	MAKE_BASE+TRUE	I326	RF_PMIC_RESET_L	30 32
3	AP_TO_BB_RST_L	MAKE_BASE+TRUE	I327	BB_RST_L	30 32
3	AP_TO_BB_WAKE_MODEM	MAKE_BASE+TRUE	I329	AP_WAKE_MODEM	35
13	BB_TO_PMU_HOST_WAKE_L	MAKE_BASE+TRUE	I328	BB_WAKE_HOST_L	30 35
13	BB_TO_AP_IPC_GPIO	MAKE_BASE+TRUE	I331	BB_IPC_GPIO	35
16	BB_TO_LEDDR_V_GSM_BLANK	MAKE_BASE+TRUE	I330	GSM_TXBURST_IND	35
3	BB_TO_AP_GPS_SYNC	MAKE_BASE+TRUE	I332	BB_GPS_SYNC	30 35

HSIC IPC

2	50_AP_BI_BB_HSIC1_DATA	MAKE_BASE+TRUE	I368	50_BB_HSIC_DATA	30 34
3	50_AP_BI_BB_HSIC1_STB	MAKE_BASE+TRUE	I369	50_BB_HSIC_STROBE	30 34
3	AP_TO_BB_HOST_RDY	MAKE_BASE+TRUE	I371	BB_HOST_RDY	30 35
3	BB_TO_AP_DEVICE_RDY	MAKE_BASE+TRUE	I370	BB_DEVICE_RDY	30 35
3	BB_TO_AP_IPC_GPIO1	MAKE_BASE+TRUE	I372	BB_IPC_GPIO1	35

UART IPC

3	AP_TO_BB_UART2_RTS_L	MAKE_BASE+TRUE	I373	BB_UART_CTS_L	30 35
3	BB_TO_AP_UART2_CTS_L	MAKE_BASE+TRUE	I374	BB_UART_RTS_L	30 35
17 3	AP_TO_BB_UART2_TXD	MAKE_BASE+TRUE	I374	BB_UART_RXD	30 35
17 3	BB_TO_AP_UART2_RXD	MAKE_BASE+TRUE	I375	BB_UART_TXD	30 35

AUDIO I2S

3	45_AP_TO_BB_I2S3_BCLK	MAKE_BASE+TRUE	I377	BB_I2S_CLK	35
3	AP_TO_BB_I2S3_DOUT	MAKE_BASE+TRUE	I378	BB_I2S_RXD	30 35
3	BB_TO_AP_I2S3_DIN	MAKE_BASE+TRUE	I379	BB_I2S_TXD	30 35
3	AP_TO_BB_I2S3_LRCLK	MAKE_BASE+TRUE	I380	BB_I2S_WS	30 35

OSCAR UART

22	OSCAR_TO_BB_UART_TXD	MAKE_BASE+TRUE	I382	BB_OTHER_RXD	30 35
22	BB_TO_OSCAR_UART_RXD	MAKE_BASE+TRUE	I381	BB_OTHER_TXD	30 35

BB DEBUG INTERFACES

3	AP_TO_BB_COREDUMP	MAKE_BASE+TRUE	I384	BB_CORE_DUMP	30 35
13	PMU_TO_BB_VBUS_DET	MAKE_BASE+TRUE	I387	BB_USB_VBUS	30 34
17	90_TRISTAR_BI_BB_USB_N	MAKE_BASE+TRUE	I386	90_BB_USB_N	30 34
17	90_TRISTAR_BI_BB_USB_P	MAKE_BASE+TRUE	I388	90_BB_USB_P	30 34

RADIO ANTENNA CONTROL

18	PP_BB_VDD_2V7	MAKE_BASE+TRUE	I389	PP_LDO14_RFSW	31 41 42
18	BB_GPIO0	MAKE_BASE+TRUE	I390	BB_LAT_GPIO0	35
18	BB_GPIO2	MAKE_BASE+TRUE	I391	BB_LAT_GPIO2	35
18	BB_GPIO3	MAKE_BASE+TRUE	I392	BB_LAT_GPIO3	35
18	BB_GPIO4	MAKE_BASE+TRUE	I394	BB_LAT_GPIO4	35

FCT TESTING

13	RADIO_TO_PMU_ADC_SMPS1	MAKE_BASE+TRUE	I395	ADC_SMPS1	30
13	RADIO_TO_PMU_ADC_PP_LDO11_VDDIO	MAKE_BASE+TRUE	I396	ADC_PP_LDO11	30
13	RADIO_TO_PMU_ADC_PP_LDO5_SIM	MAKE_BASE+TRUE	I398	ADC_PP_LDO5	30
13	RADIO_TO_PMU_ADC_SMPS4	MAKE_BASE+TRUE	I397	ADC_SMPS4	30

UPPER RADIO ANTENNA CONTROL

25	50_AP_WIFI_5G_CONN_ANT	MAKE_BASE+TRUE	I410	50_WIFI_5G_CONN_ANT	50
25	50_AP_UAT_FEED	MAKE_BASE+TRUE	I409	50_UPPER_ANT_FEED	50
	UAT_ANT_GND	MAKE_BASE+TRUE	I411	ANT_GND	50
29 26 17 15 12	PP3V0_TRISTAR	MAKE_BASE+TRUE	I404	PAC_VDD_3V0	53
25 8	NORTH_AC_GND_SCREW	MAKE_BASE+TRUE	I412	NORTH_ANT_GND	50

POWER

26 17 15 14 13 12 10 4 3	PP1V8_SDRAM	MAKE_BASE+TRUE	I314	PP_WL_BT_VDDIO_AP	51
		MAKE_BASE+TRUE	I315	PP_STOCKHOLM_IVR_S2P	52 54
		MAKE_BASE+TRUE	I407	REFE_VIO_S2P	53

WLAN/BT HOUSE KEEPING

13	45_PMU_TO_WLAN_CLK32K	MAKE_BASE+TRUE	I316	CLK32K_AP	30 51
13	PMU_TO_WLAN_REG_ON	MAKE_BASE+TRUE	I317	WLAN_REG_ON	30 51
13	WLAN_TO_PMU_HOST_WAKE	MAKE_BASE+TRUE	I318	HOST_WAKE_WLAN	30 51
13	PMU_TO_BT_REG_ON	MAKE_BASE+TRUE	I319	BT_REG_ON	30 51
3	AP_TO_BT_WAKE	MAKE_BASE+TRUE	I320	WAKE_BT	30 51
13	BT_TO_PMU_HOST_WAKE	MAKE_BASE+TRUE	I321	HOST_WAKE_BT	51

3	AP_TO_WLAN_JTAG_SWCLK	MAKE_BASE+TRUE	I333	WLAN_JTAG_SWCLK	30 51
3	AP_TO_WLAN_JTAG_SWDIO	MAKE_BASE+TRUE	I334	WLAN_JTAG_SWDIO	30 51
13	WLAN_TO_PMU_PCIE_WAKE_L	MAKE_BASE+TRUE	I335	WLAN_PCIE_WAKE_L	30 51
3	AP_TO_WLAN_DEVICE_WAKE	MAKE_BASE+TRUE	I336	PCIE_DEV_WAKE	30 51
3	90_WLAN_TO_AP_PCIE1_RXDP_P	MAKE_BASE+TRUE	I337	90_WLAN_PCIE_TDP	30 51
3	90_WLAN_TO_AP_PCIE1_RXDP_N	MAKE_BASE+TRUE	I338	90_WLAN_PCIE_TDN	30 51
3	90_AP_TO_WLAN_PCIE1_TXDP_P	MAKE_BASE+TRUE	I338	90_WLAN_PCIE_RDP	30 51
3	90_AP_TO_WLAN_PCIE1_TXDP_N	MAKE_BASE+TRUE	I339	90_WLAN_PCIE_RDN	30 51
3	90_AP_TO_WLAN_PCIE1_REFCLK1_P	MAKE_BASE+TRUE	I342	90_WLAN_PCIE_REFCLK_P	51
3	90_AP_TO_WLAN_PCIE1_REFCLK1_N	MAKE_BASE+TRUE	I341	90_WLAN_PCIE_REFCLK_N	51
3	WLAN_TO_AP_PCIE1_CLKREQ_L	MAKE_BASE+TRUE	I344	WLAN_PCIE_CLKREQ_L	30 51
3	AP_TO_WLAN_PCIE1_RST_L	MAKE_BASE+TRUE	I343	WLAN_PCIE_PERST_L	30 51

WLAN HSIC IPC

3	WLAN_TO_AP_UART4_RXD	MAKE_BASE+TRUE	I345	WLAN_UART_TXD	30 51
3	AP_TO_WLAN_UART4_TXD	MAKE_BASE+TRUE	I348	WLAN_UART_RXD	30 51
3	WLAN_TO_AP_UART4_CTS_L	MAKE_BASE+TRUE	I347	WLAN_UART_RTS_L	30 51
3	AP_TO_WLAN_UART4_RTS_L	MAKE_BASE+TRUE	I346	WLAN_UART_CTS_L	30 51

BT UART IPC

3	AP_TO_BT_UART1_RTS_L	MAKE_BASE+TRUE	I349	BT_UART_CTS_L	51
3	BT_TO_AP_UART1_CTS_L	MAKE_BASE+TRUE	I352	BT_UART_RTS_L	51
3	AP_TO_BT_UART1_TXD	MAKE_BASE+TRUE	I351	BT_UART_RXD	30 51
3	BT_TO_AP_UART1_RXD	MAKE_BASE+TRUE	I350	BT_UART_TXD	30 51

BT AUDIO PCM


3	45_AP_TO_BT_I2S1_BCLK	MAKE_BASE+TRUE	I354	BT_PCM_CLK	51
3	AP_TO_BT_I2S1_DOUT	MAKE_BASE+TRUE	I353	BT_PCM_IN	51
3	BT_TO_AP_I2S1_DIN	MAKE_BASE+TRUE	I355	BT_PCM_OUT	51
3	AP_TO_BT_I2S1_LRCLK	MAKE_BASE+TRUE	I356	BT_PCM_SYNC	51

OSCAR STATES

22	OSCAR_TO_RADIO_CONTEXT_A	MAKE_BASE+TRUE	I358	OSCAR_CONTEXT_A	51
22	OSCAR_TO_RADIO_CONTEXT_B	MAKE_BASE+TRUE	I357	OSCAR_CONTEXT_B	51

STOCKHOLM

3	STOCKHOLM_TO_AP_UART3_CTS_L	MAKE_BASE+TRUE	I359	STOCKHOLM_RTS_L	30 52
3	AP_TO_STOCKHOLM_UART3_RTS_L	MAKE_BASE+TRUE	I360	STOCKHOLM_CTS_L	30 52
3	STOCKHOLM_TO_AP_UART3_RXD	MAKE_BASE+TRUE	I361	STOCKHOLM_UART_TXD	30 52
3	AP_TO_STOCKHOLM_UART3_TXD	MAKE_BASE+TRUE	I362	STOCKHOLM_UART_RXD	30 52
3	AP_TO_STOCKHOLM_DWLD_REQ	MAKE_BASE+TRUE	I362	STOCKHOLM_FW_DWLD_REQ	52
13	STOCKHOLM_TO_PMU_HOST_WAKE	MAKE_BASE+TRUE	I364	STOCKHOLM_HOST_WAKE	30 52
3	AP_TO_STOCKHOLM_EN	MAKE_BASE+TRUE	I365	STOCKHOLM_ENABLE	52
29 26 17 15 12	PP3V0_TRISTAR	MAKE_BASE+TRUE	I366	STOCKHOLM_VDD_MUX_3V0	54
3	AP_TO_STOCKHOLM_SIM_SEL	MAKE_BASE+TRUE	I367	STOCKHOLM_SIM_SEL	54
25	AP_TO_STOCKHOLM_ANT	MAKE_BASE+TRUE	I406	STOCKHOLM_ANT	52

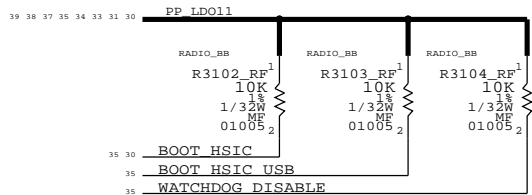
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AP INTERFACE & DEBUG CONNECTORS

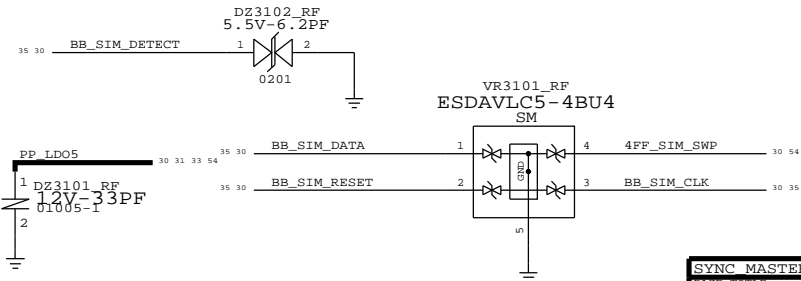
PROBE POINTS

PP3105_RF P2MM-SM 1 CLK32K_AP 29 51	PP3121_RF P2MM-NSM 1 STOCKHOLM_HOST_WAKE 29 52	PP3115_RF P4MM-NSM 1 50_BB_HSIC_STROBE 29 34	PP3130_RF P4MM-SM 1 BB_JTAG_RST_L 34	PP3141_RF P4MM-SM 1 BB_UART_TXD 29 35	PP3170_RF P4MM-SM 1 RFFE1_CLK 35 39 40 41 42 43 44
PP3113_RF P4MM-SM 1 BB_COEX_UART_RXD 35 51	PP3122_RF P4MM-SM 1 BB_REQUEST_XO_CLK 32 52	PP3116_RF P4MM-NSM 1 50_BB_HSIC_DATA 29 34	PP3131_RF P4MM-SM 1 BB_JTAG_TCK 34	PP3142_RF P4MM-SM 1 BB_UART_RXD 29 35	PP3171_RF P4MM-SM 1 RFFE1_DATA 35 39 40 41 42 43 44
PP3114_RF P4MM-SM 1 BB_COEX_UART_TXD 35 51	PP3123_RF P2MM-NSM 1 STOCKHOLM_UART_RXD 29 52	PP3101_RF P4MM-SM 1 BB_DEBUG_ERROR 35	PP3132_RF P4MM-SM 1 BB_JTAG_TMS 34	PP3143_RF P4MM-SM 1 BB_UART_RTS_L 29 35	PP3172_RF P4MM-SM 1 RFFE2_CLK 35 45 46 48
PP3119_RF P2MM-SM 1 BT_UART_TXD 29 51	PP3124_RF P2MM-SM 1 STOCKHOLM_UART_TXD 29 52	PP3102_RF P4MM-SM 1 RF_PMIC_RESET_L 29 32	PP3133_RF P4MM-SM 1 BB_JTAG_TDO 34	PP3144_RF P4MM-SM 1 BB_UART_CTS_L 29 35	PP3173_RF P4MM-SM 1 RFFE2_DATA 35 45 46 48
PP3120_RF P2MM-NSM 1 BT_UART_RXD 29 51	PP3125_RF P2MM-NSM 1 STOCKHOLM_CTS_L 29 52	PP3103_RF P4MM-SM 1 PS_HOLD_PMIC 32	PP3134_RF P4MM-SM 1 BB_JTAG_TDI 34	PP3145_RF P4MM-SM 1 BB_HOST_RDY 29 35	PP3175_RF P4MM-SM 1 BB_I2S_WS 29 35
PP3152_RF P2MM-SM 1 WAKE_BT 29 51	PP3126_RF P2MM-NSM 1 STOCKHOLM_RTS_L 29 52	PP3127_RF P4MM-SM 1 PMIC_RESOUT_L 32 34	PP3135_RF P4MM-SM 1 BB_JTAG_TEST_L 34	PP3146_RF P4MM-SM 1 BB_DEVICE_RDY 29 35	PP3176_RF P4MM-SM 1 BB_I2S_RXD 29 35
PP3153_RF P4MM-SM 1 WLAN_REG_ON 29 51	PP3128_RF P4MM-SM 1 PP_PN65_VCC_SIM 52	PP3104_RF P4MM-SM 1 MDM_CLK 32 34	PP3136_RF P4MM-SM 1 BB_DEBUG_STATUS 35	PP3147_RF P4MM-SM 1 BB_GPS_SYNC 29 35	PP3177_RF P4MM-SM 1 BB_I2S_TXD 29 35
PP3154_RF P4MM-SM 1 BT_REG_ON 29 51	PP3144_RF P4MM-SM 1 STOCKHOLM_SIM_SWP 52 54	PP3109_RF P4MM-SM 1 PP_LDO11 30 31 33 34 35 37 38	PP3137_RF P4MM-SM 1 BB_CORE_DUMP 29 35	PP3148_RF P4MM-SM 1 BB_WAKE_HOST_L 29 35	PP3178_RF P4MM-SM 1 BB_OTHER_TXD 29 35
PP3155_RF P2MM-SM 1 HOST_WAKE_WLAN 29 51	PP3129_RF P4MM-SM 1 REF_CLK_FROM_BB 32 52	PP3110_RF P4MM-SM 1 RADIO_ON_L 29 32	PP3138_RF P4MM-SM 1 BB_USB_VBUS 29 34	PP3149_RF P4MM-SM 1 BB_RESET_DET_L 29 35	PP3179_RF P4MM-SM 1 BB_OTHER_RXD 29 35
PP3156_RF P2MM-SM 1 WLAN_PCIE_WAKE_L 29 51	PP3165_RF P4MM-SM 1 DSDS_SIM_CLK 34 54	PP3111_RF P4MM-SM 1 SPMI_DATA 32 34	PP3139_RF P4MM-SM 1 90_BB_USB_N 29 34	PP3150_RF P4MM-SM 1 BB_RST_L 29 32	
PP3157_RF P2MM-SM 1 WLAN_PCIE_PERST_L 29 51	PP3183_RF P4MM-SM 1 DSDS_SIM_RESET 34 54	PP3112_RF P4MM-SM 1 SPMI_CLK 32 34	PP3140_RF P4MM-SM 1 90_BB_USB_P 29 34	PP3151_RF P4MM-SM 1 BOOT_HSIC 30 35	
PP3158_RF P4MM-SM 1 WLAN_PCIE_CLKREQ_L 29 51	PP3184_RF P4MM-SM 1 DSDS_SIM_DATA 34 54				
PP3159_RF P4MM-SM 1 PCIE_DEV_WAKE 29 51	PP3186_RF P4MM-SM 1 DSDS_SIM_DETECT 34				
PP3160_RF P4MM-SM 1 WLAN_UART_RTS_L 29 51	PP3187_RF P4MM-SM 1 PP_LDO6 31 33 54				
PP3161_RF P4MM-SM 1 WLAN_UART_CTS_L 29 51	PP3188_RF P4MM-SM 1 DSDS_SIM_SWP 54				
PP3162_RF P4MM-SM 1 WLAN_UART_RXD 29 51	PP3189_RF P4MM-SM 1 DSDS_SIM_DATA_R 54				
PP3163_RF P4MM-SM 1 WLAN_UART_TXD 29 51					
PP3190_RF P4MM-SM 1 WLAN_JTAG_SWDCCLK 29 51	PP 3178_RF P2MM-NSM 1 BB_SIM_RESET 30 35				
PP3191_RF P4MM-SM 1 WLAN_JTAG_SWDIO 29 51	PP 3179_RF P2MM-NSM 1 BB_SIM_CLK 30 35				
	PP 3180_RF P2MM-NSM 1 BB_SIM_DATA 30 35				
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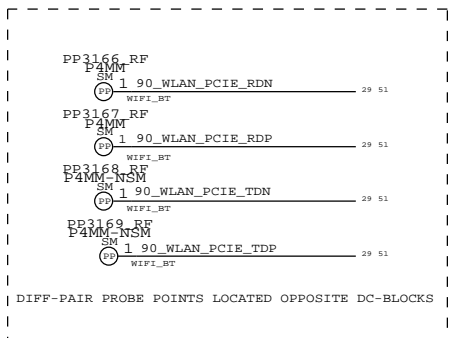
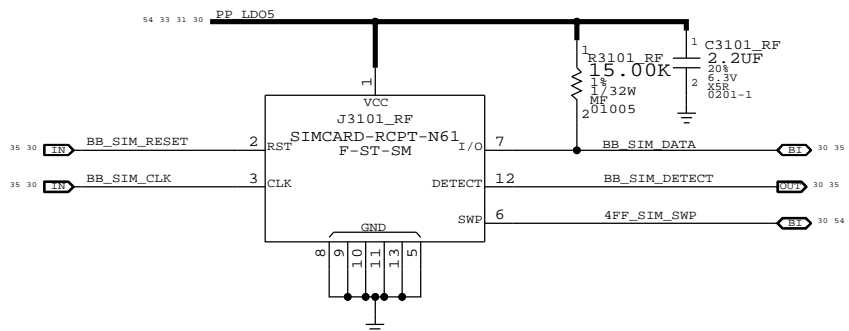
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197S0598	197S0593	ALTERNATE	Y3301_RF	AVX 19.2MHZ XTAL
138S00005	138S00003	ALTERNATE	C3216_RF	150F CAPACITOR
138S0739	138S0706	ALTERNATE	C4207_RF	1.00UF CAPACITOR
138S0945	138S0706	ALTERNATE	C4207_RF	1.00UF CAPACITOR
138S1103	138S0719	ALTERNATE	C4007_RF	4.7UF CAPACITOR
339S0231	339S0228	ALTERNATE	U5201_RF	CORONA MODULE USI
339S0242	339S0228	ALTERNATE	U5201_RF	CORONA MODULE TDK
155S00024	155S0950	ALTERNATE	F_TRI_RF	TRIPLEXER BIN2



SIM CARD ESD PROTECTION



SIM CARD CONNECTOR

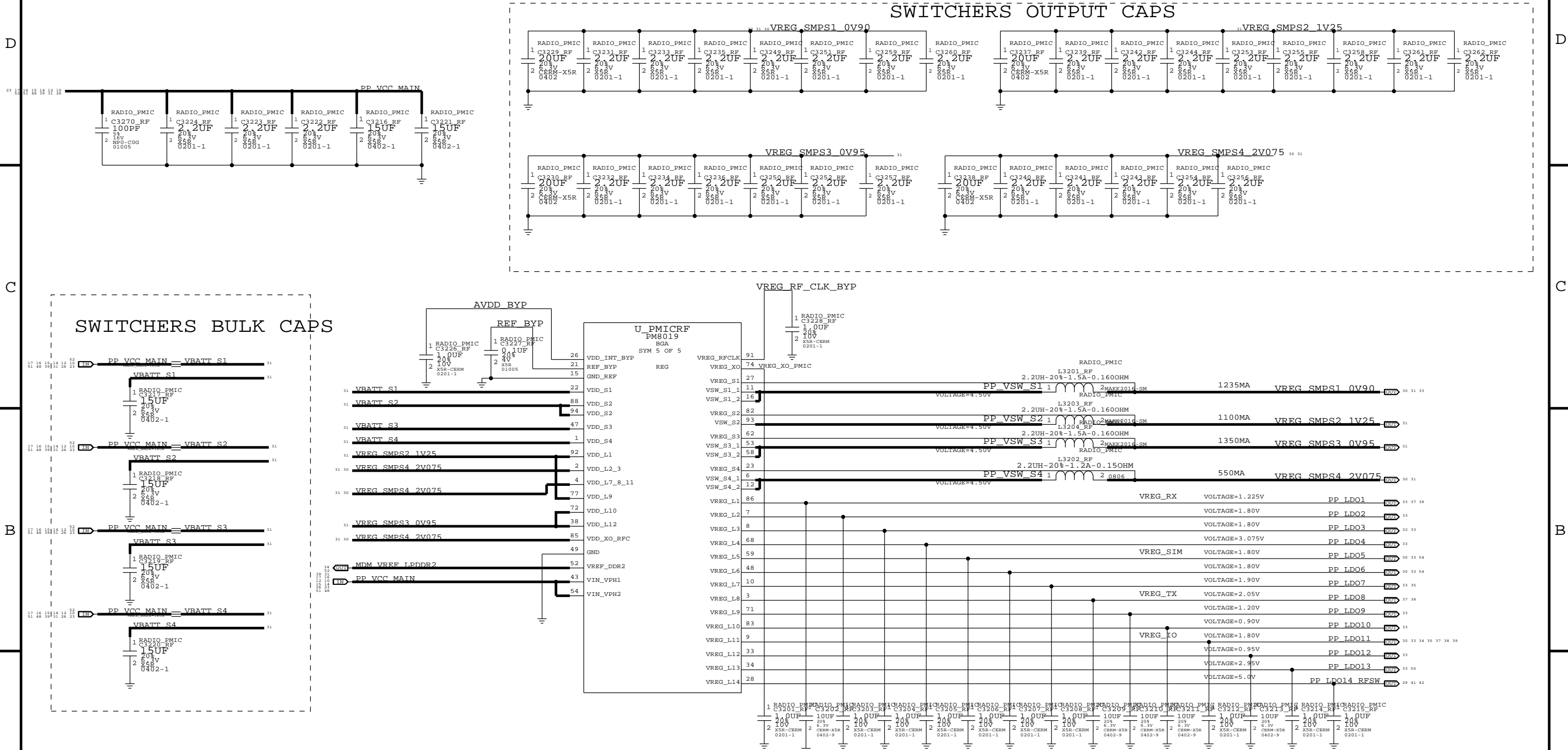



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BASEBAND PMU (1 OF 2)

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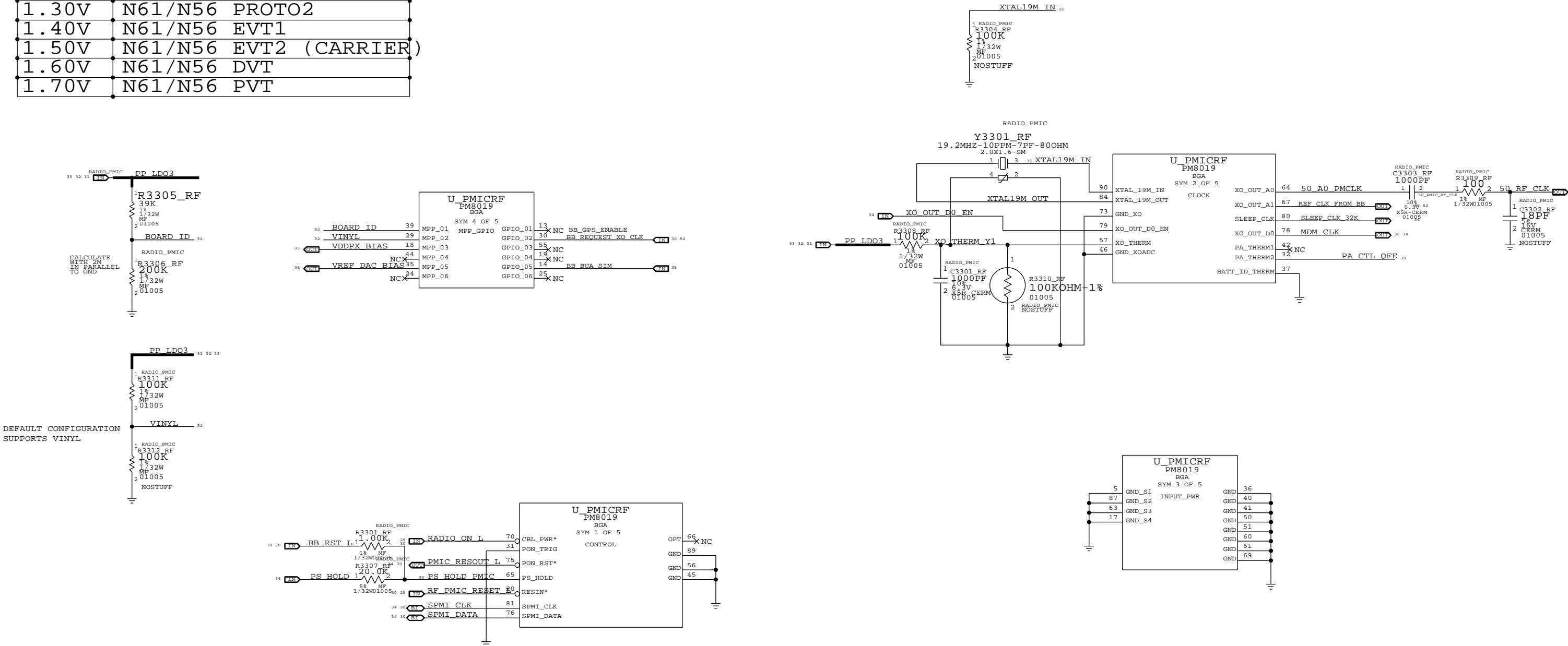
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BASEBAND PMU (2 OF 2)

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C401
R411
L400
U404

BOARD ID	REVISION
0.00V	N61 PROTO_MLB1
0.50V	N61 DEV3
0.70V	N61 DEV4
0.90V	N61 PROTO_MLB2
1.10V	N61/N56 PROTO1
1.30V	N61/N56 PROTO2
1.40V	N61/N56 EVT1
1.50V	N61/N56 EVT2 (CARRIER)
1.60V	N61/N56 DVT
1.70V	N61/N56 PVT



BASEBAND PMU (2 OF 2)			
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C538
R500
L500
U502

D

C

B

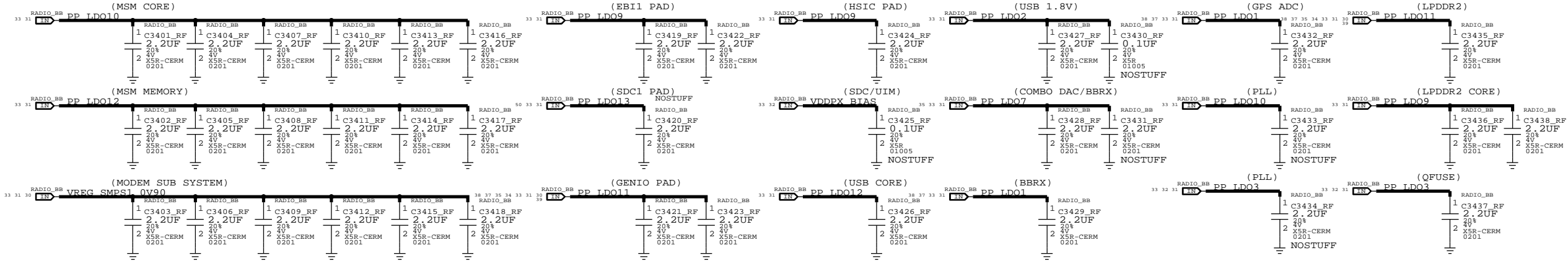
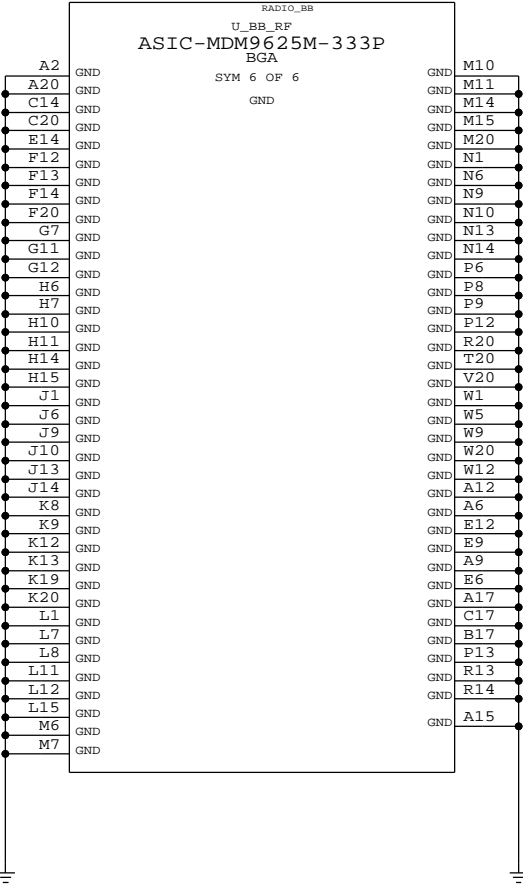
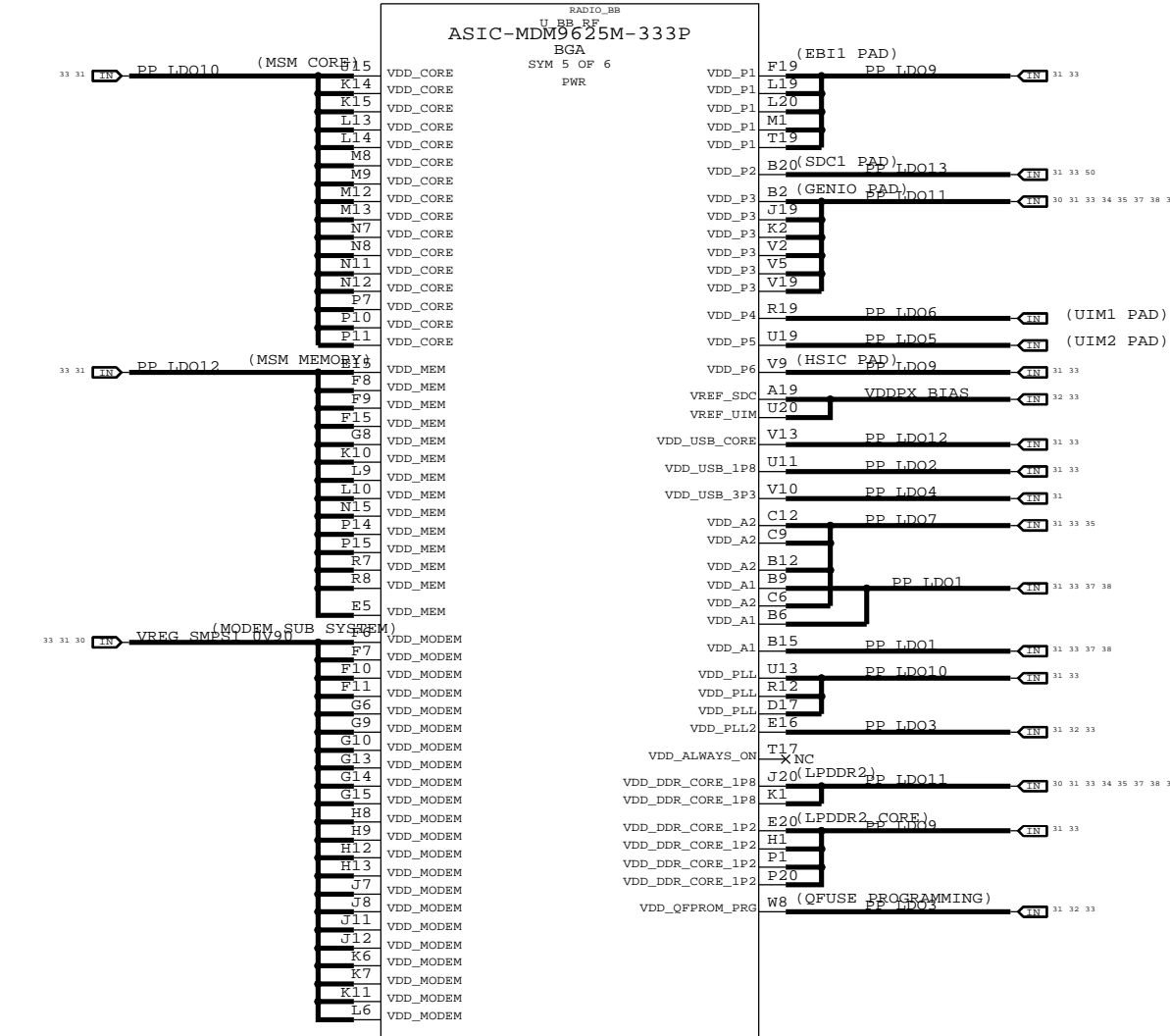
A

D

C

B

A

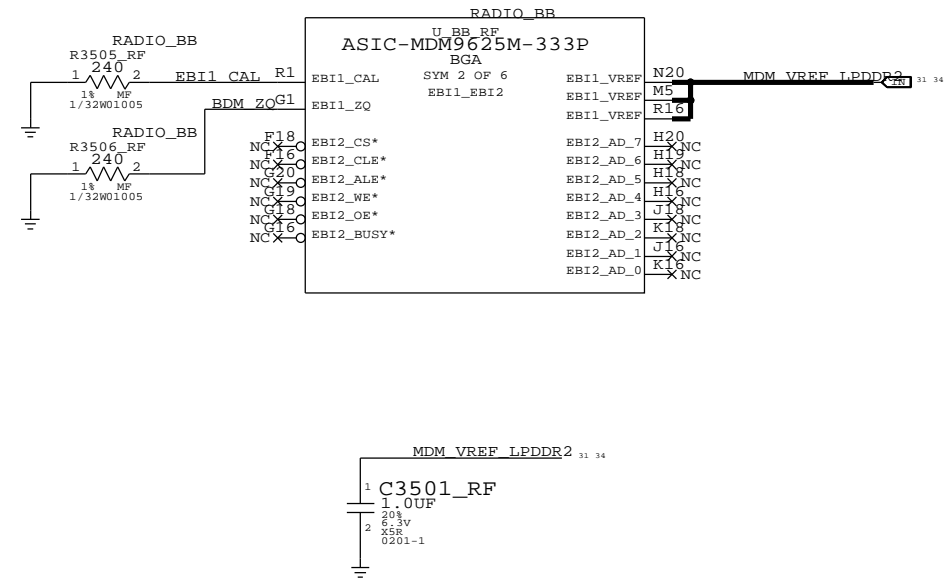
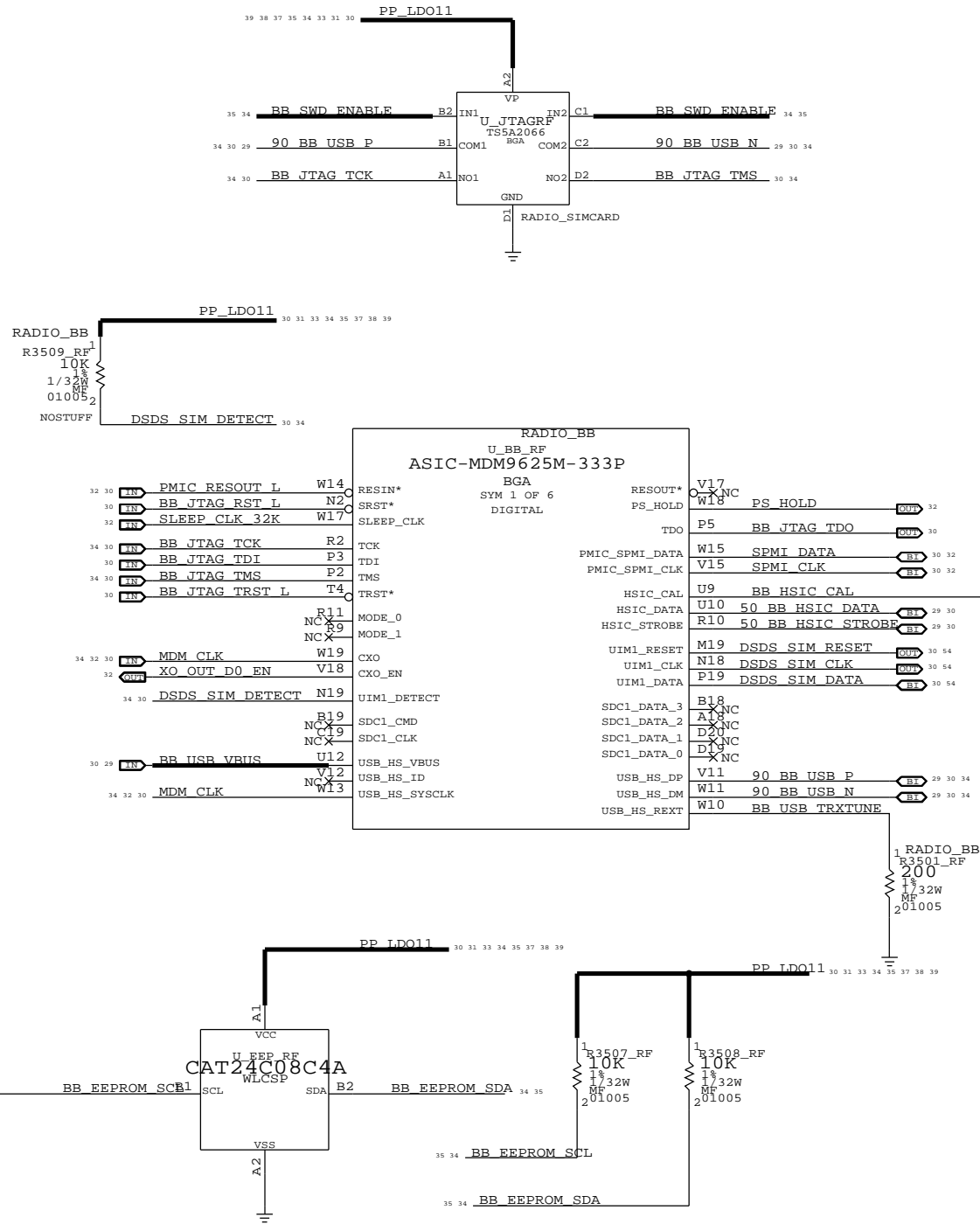


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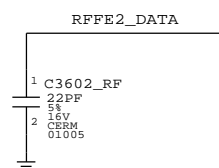
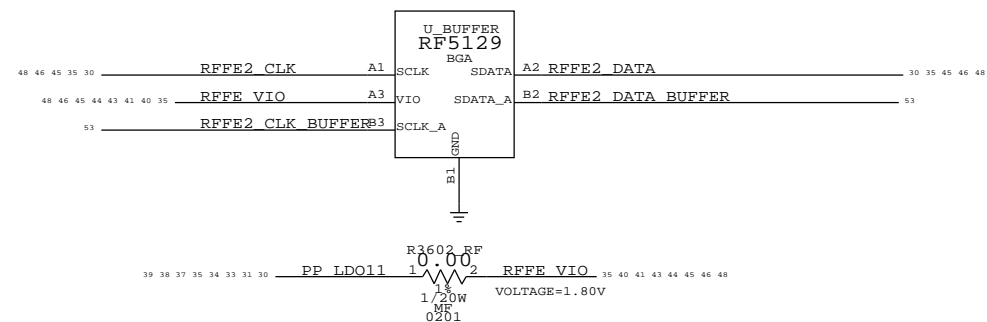
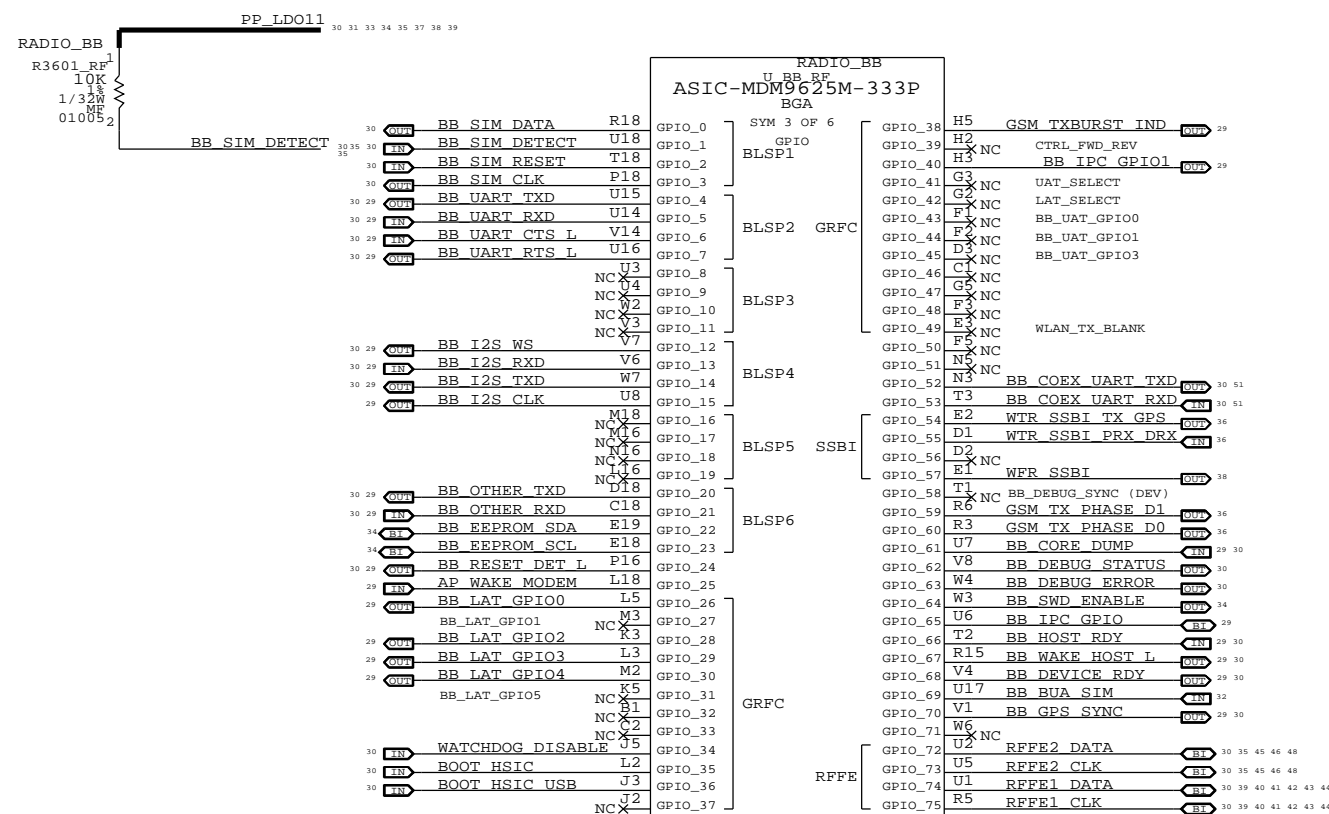
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C600
R606
L600
U602



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C704
R700
L700
U702



MOBILE DATA MODEM (2 OF 2)



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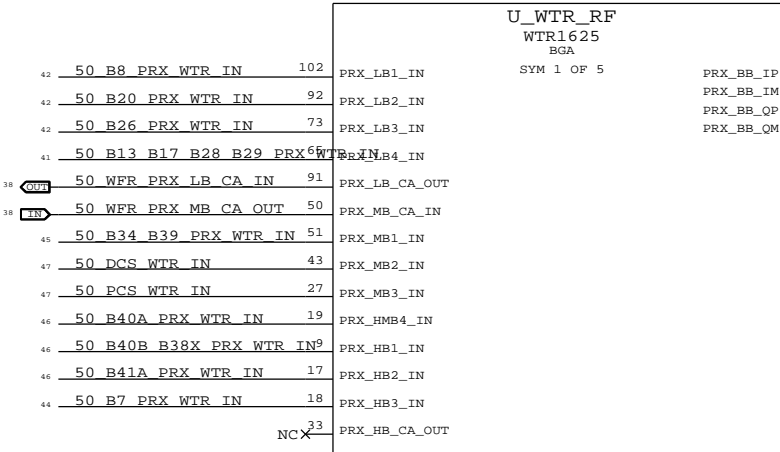
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WTR TRANSCEIVER (1 OF 2)

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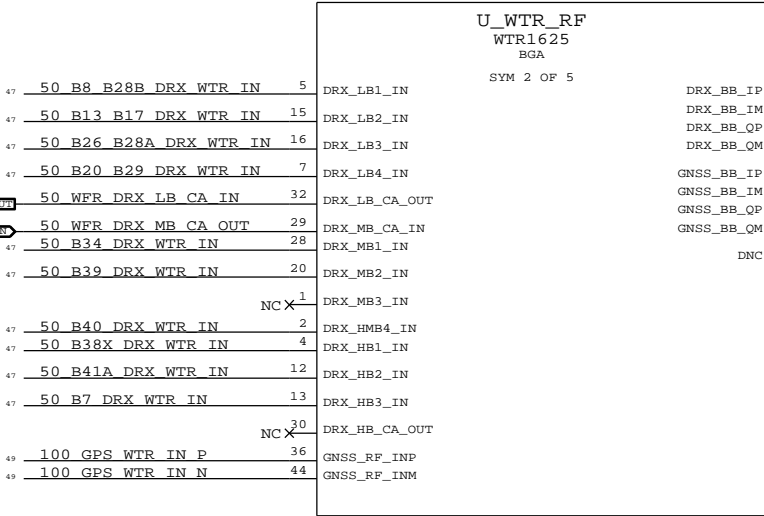
C802
R802
L800
U803

LB1	DC
LB2	DC
LB3	DC
LB4	DC
MB1	NO DC
MB2	DC
MB3	DC
HB1	NO DC
HB2	DC
HB3	DC
HMB4	NO DC

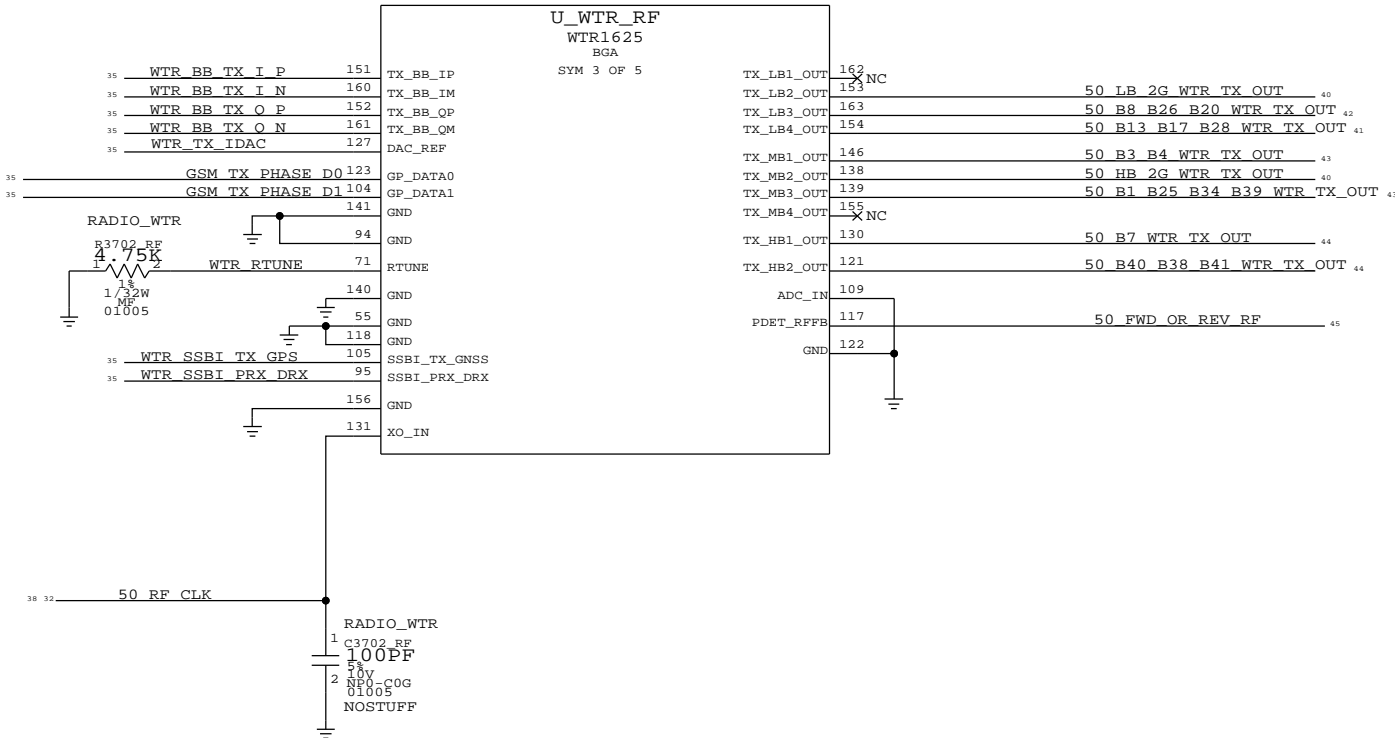


99	WTR_BB_PRX_I_P	35
108	WTR_BB_PRX_I_N	35
107	WTR_BB_PRX_O_P	35
97	WTR_BB_PRX_O_N	35

LB1	DC
LB2	DC
LB3	DC
LB4	DC
MB1	NO DC
MB2	DC
MB3	DC
HB1	NO DC
HB2	DC
HB3	DC
HMB4	NO DC




76	WTR_BB_DRX_I_P	RADIO_WTR
86	WTR_BB_DRX_I_N	RADIO_WTR
61	WTR_BB_DRX_O_P	RADIO_WTR
68	WTR_BB_DRX_O_N	RADIO_WTR
60	WTR_BB_GPS_I_P	RADIO_WTR
53	WTR_BB_GPS_I_N	RADIO_WTR
67	WTR_BB_GPS_O_P	RADIO_WTR
85	WTR_BB_GPS_O_N	RADIO_WTR



RF_CLK IS SHARED BETWEEN WTR AND WFR. LENGTH DIFFERENCE BETWEEN THE TWO SHOULD BE < 5MM.

RF TRANSCEIVER (1 OF 3)

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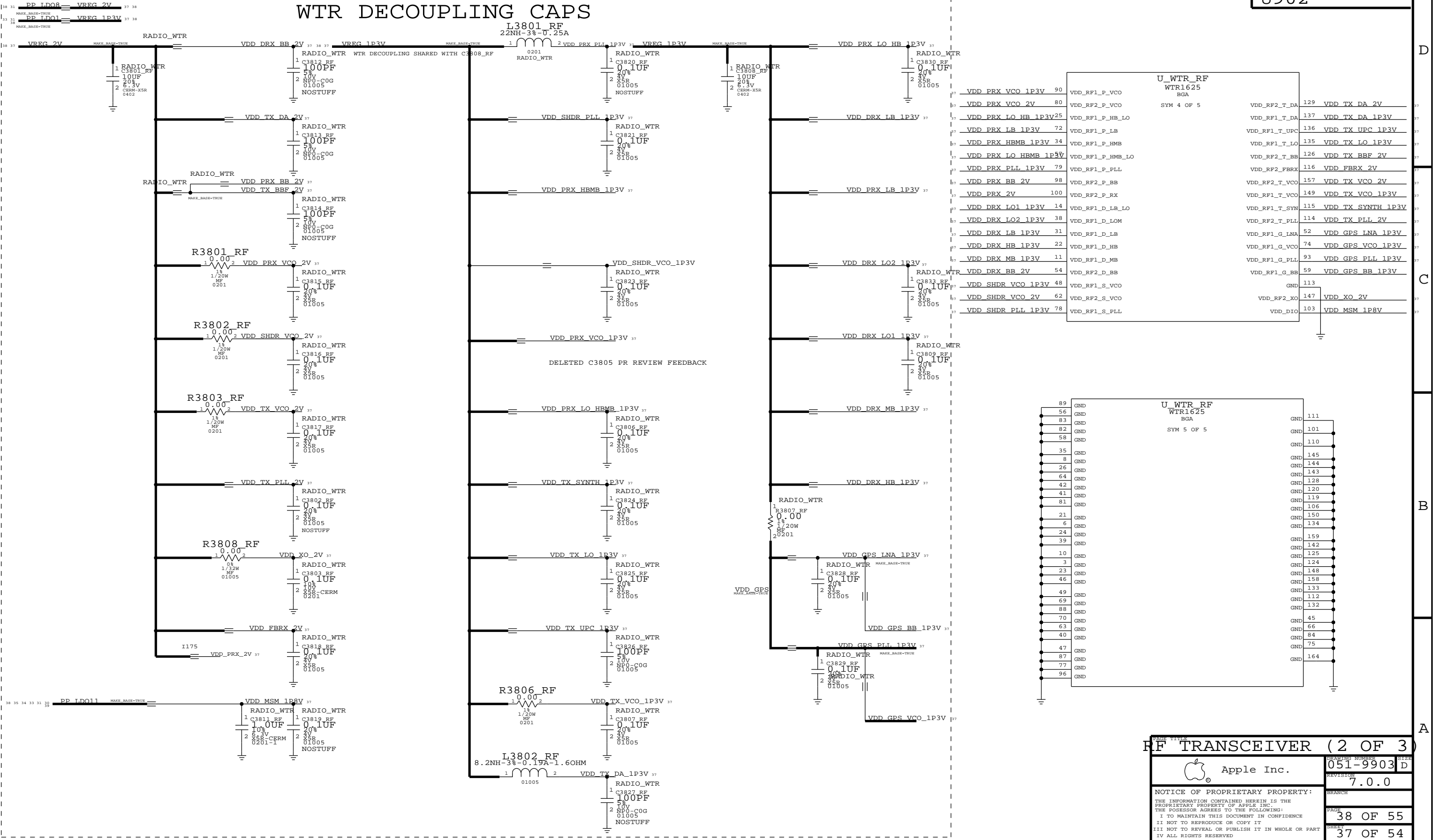
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C934
R926
L3802_RF
U902

WTR DECOUPLING CAPS

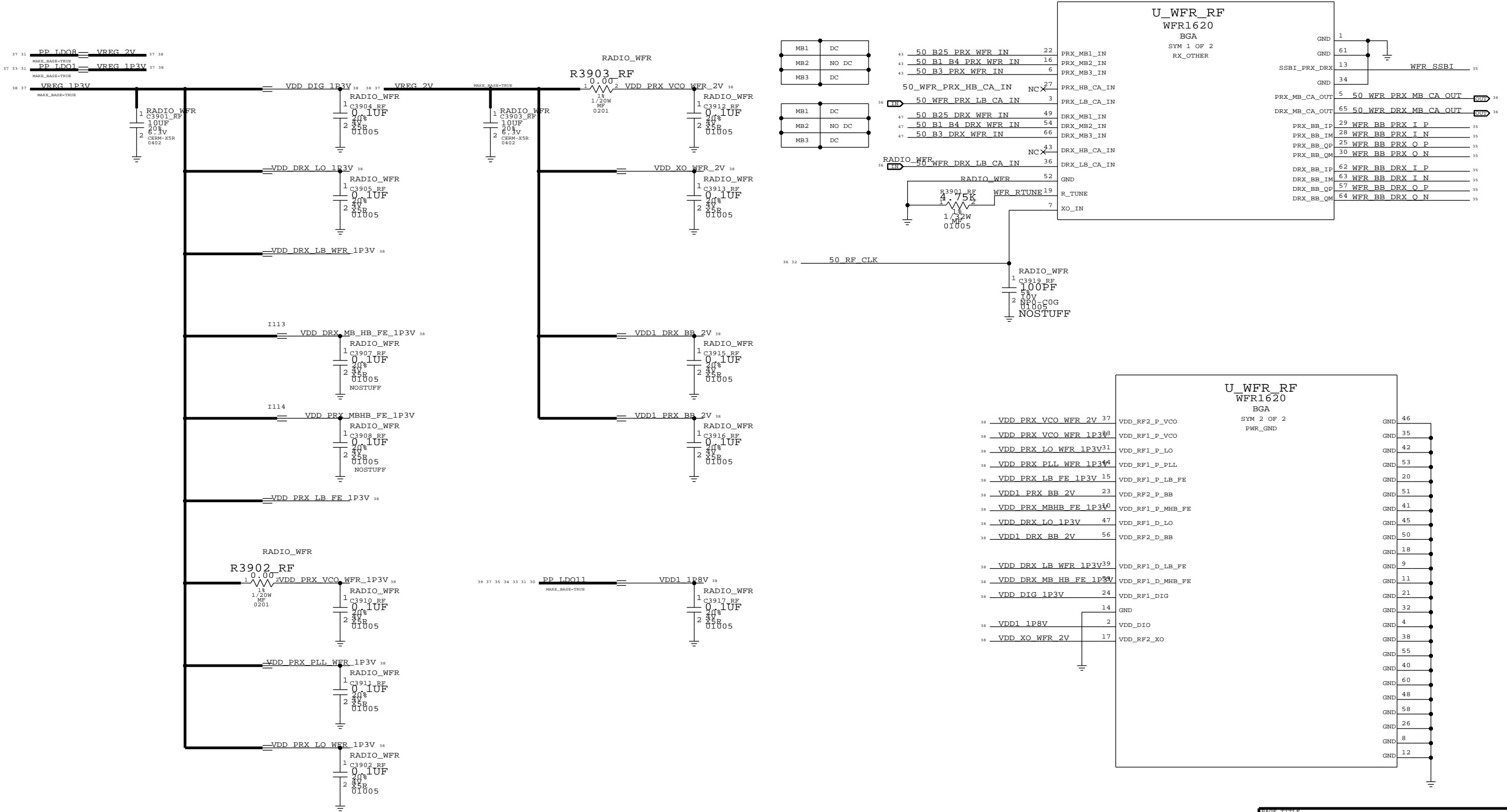
L3801 RF
22NH-3%-0.25A




WFR TRANSCEIVER

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C1019
R1016
L1000
U1002



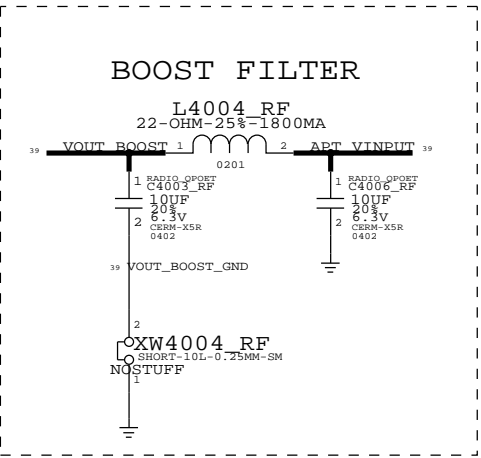
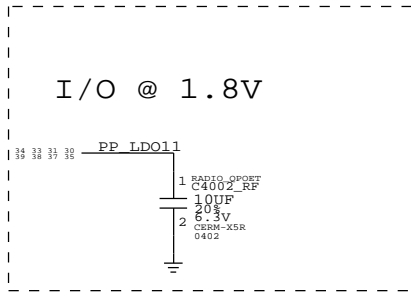
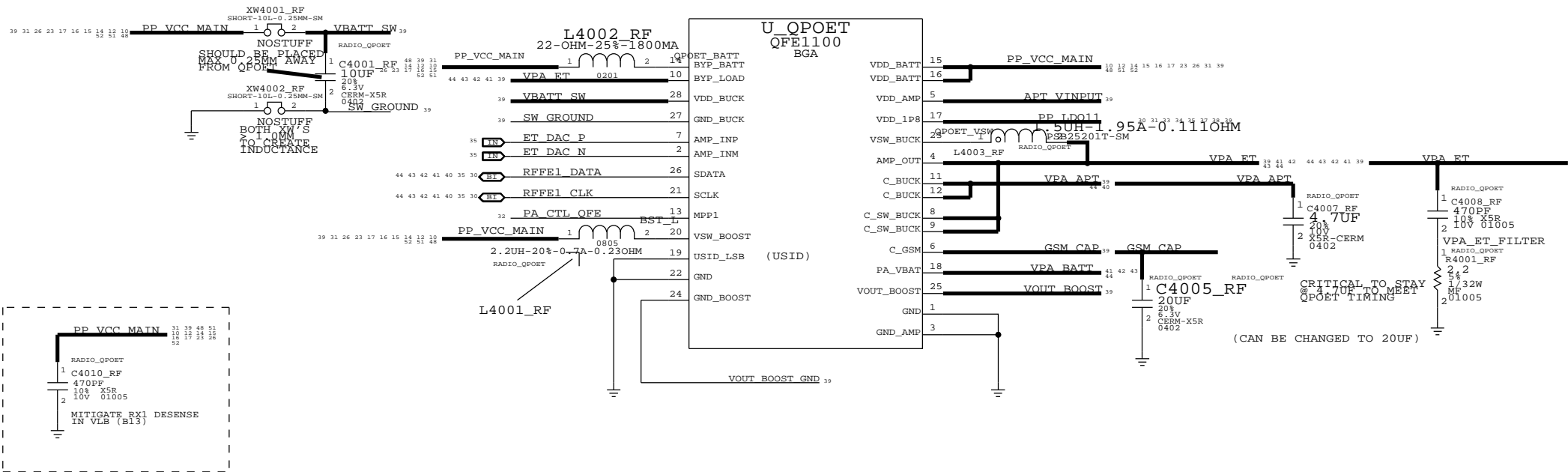
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QFE DCDC

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C1110
R1102
L1104
U1101

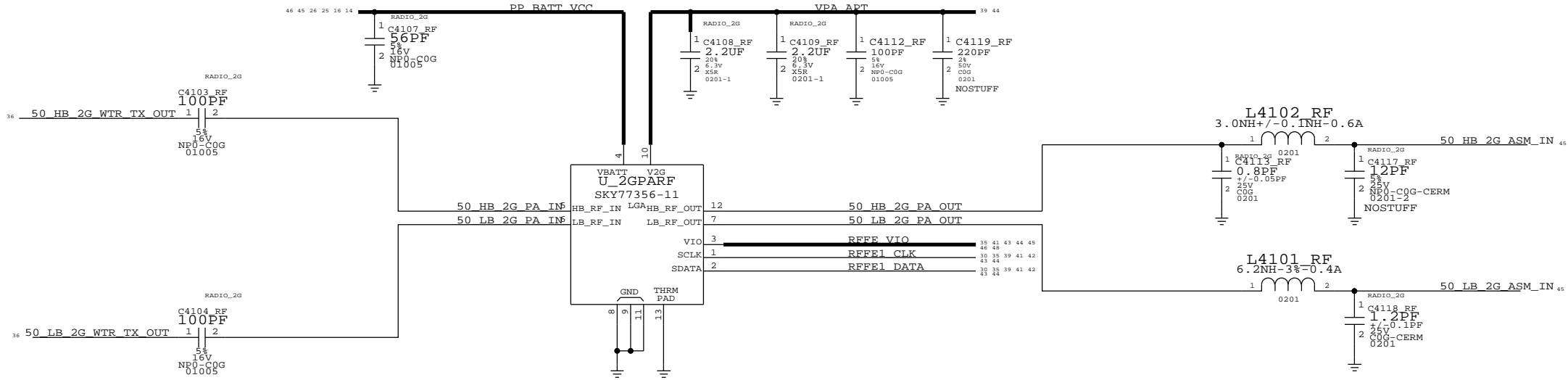



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2G PA

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C1208
R1200
L1204
U1201



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VERY LOW BAND PAD (B13, B17, B28)

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C1332
R1300
L4215_RF
U1304

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C

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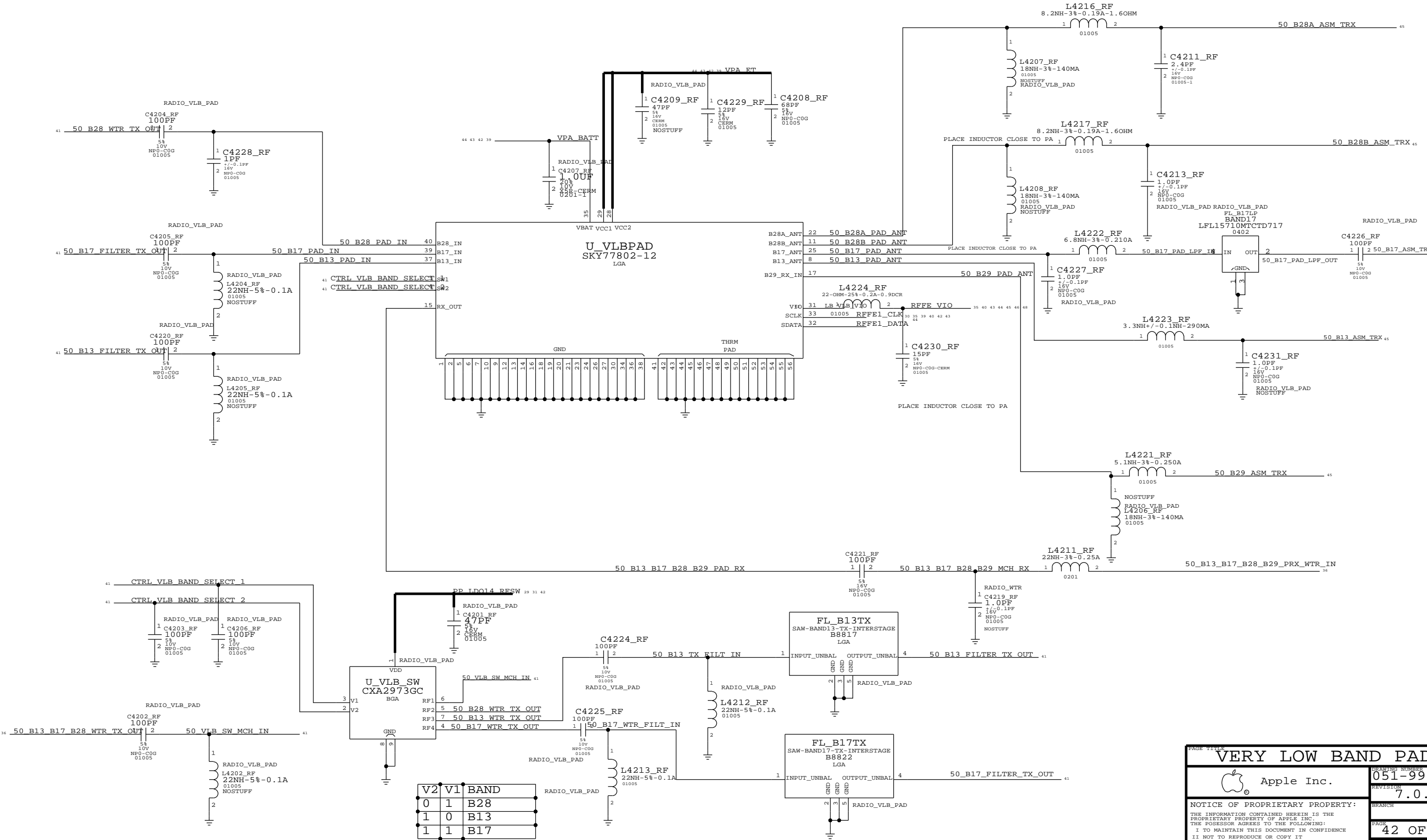
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LOW BAND PAD (B8, B26, B20)

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C4318_RF
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U1402

D

C

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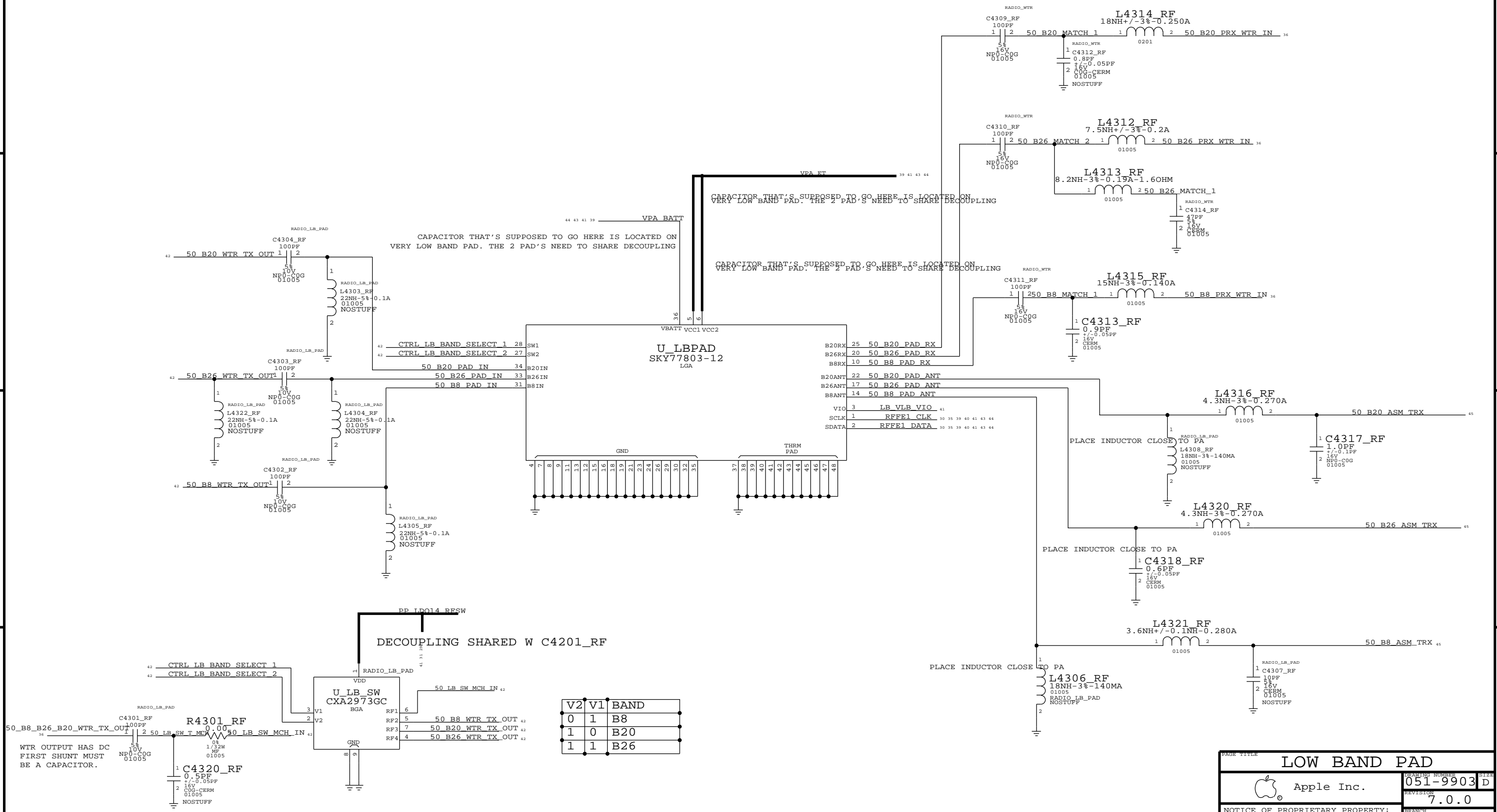
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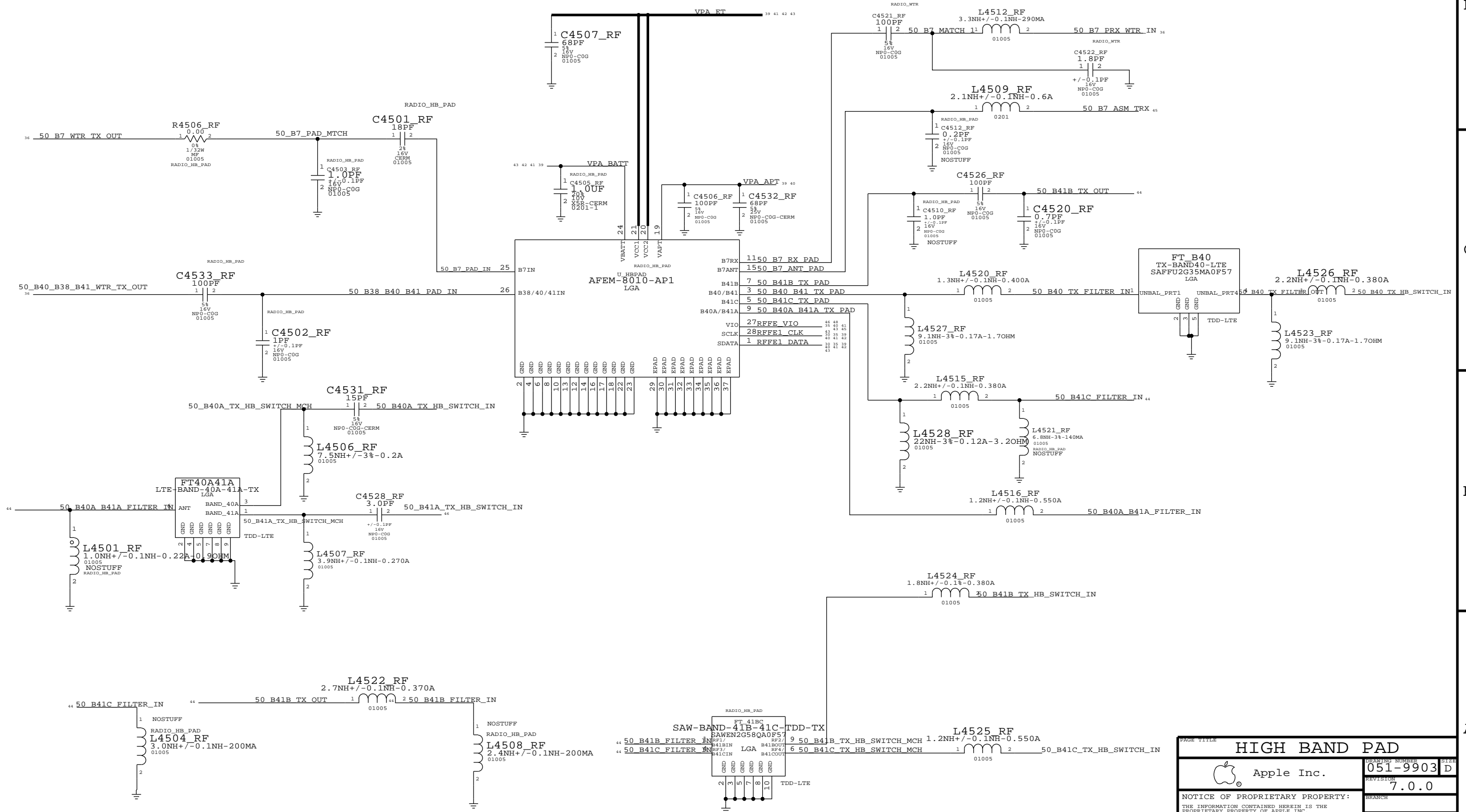
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HIGH BAND PAD (B7, B38, B40, B41, XGP)

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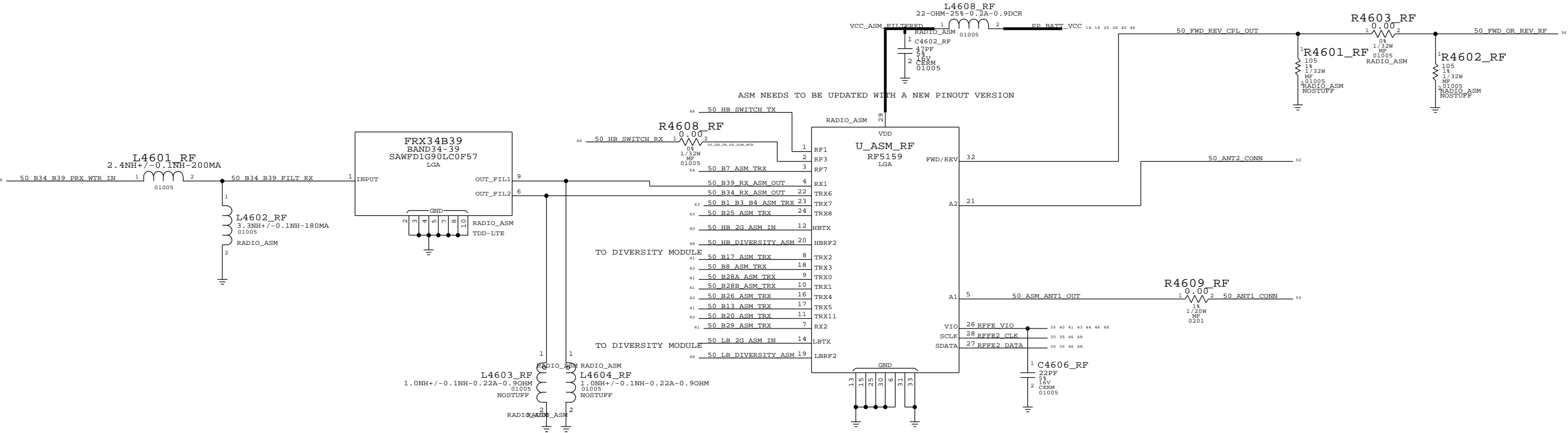
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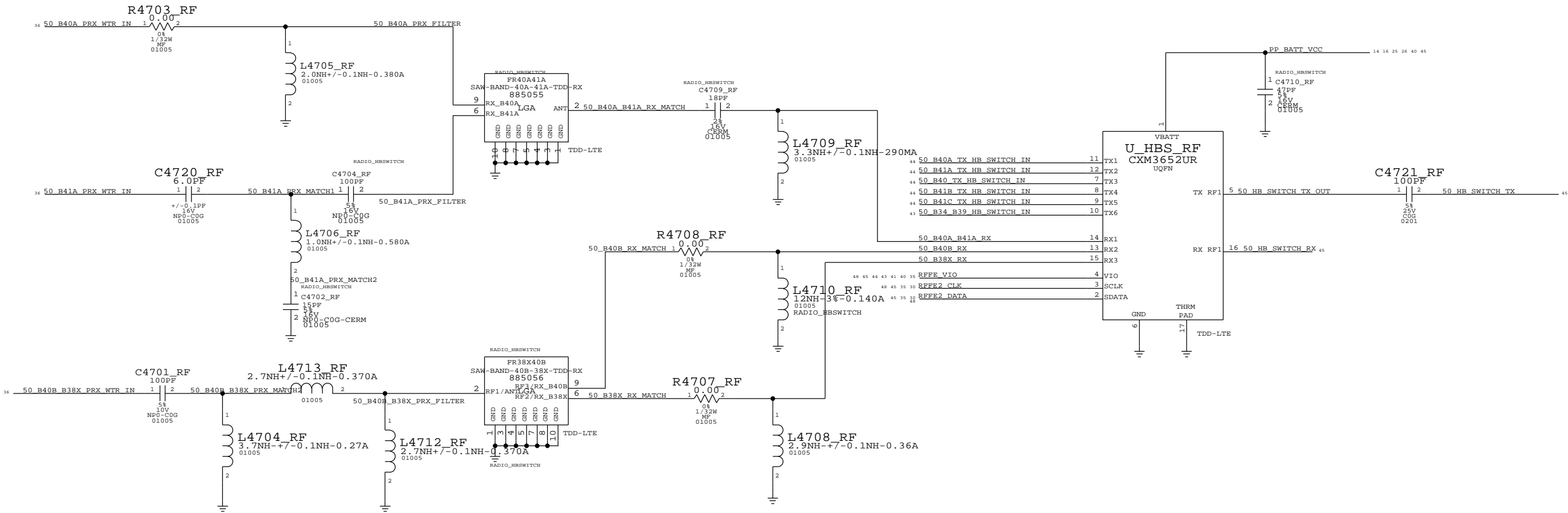
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HIGH BAND SWITCH

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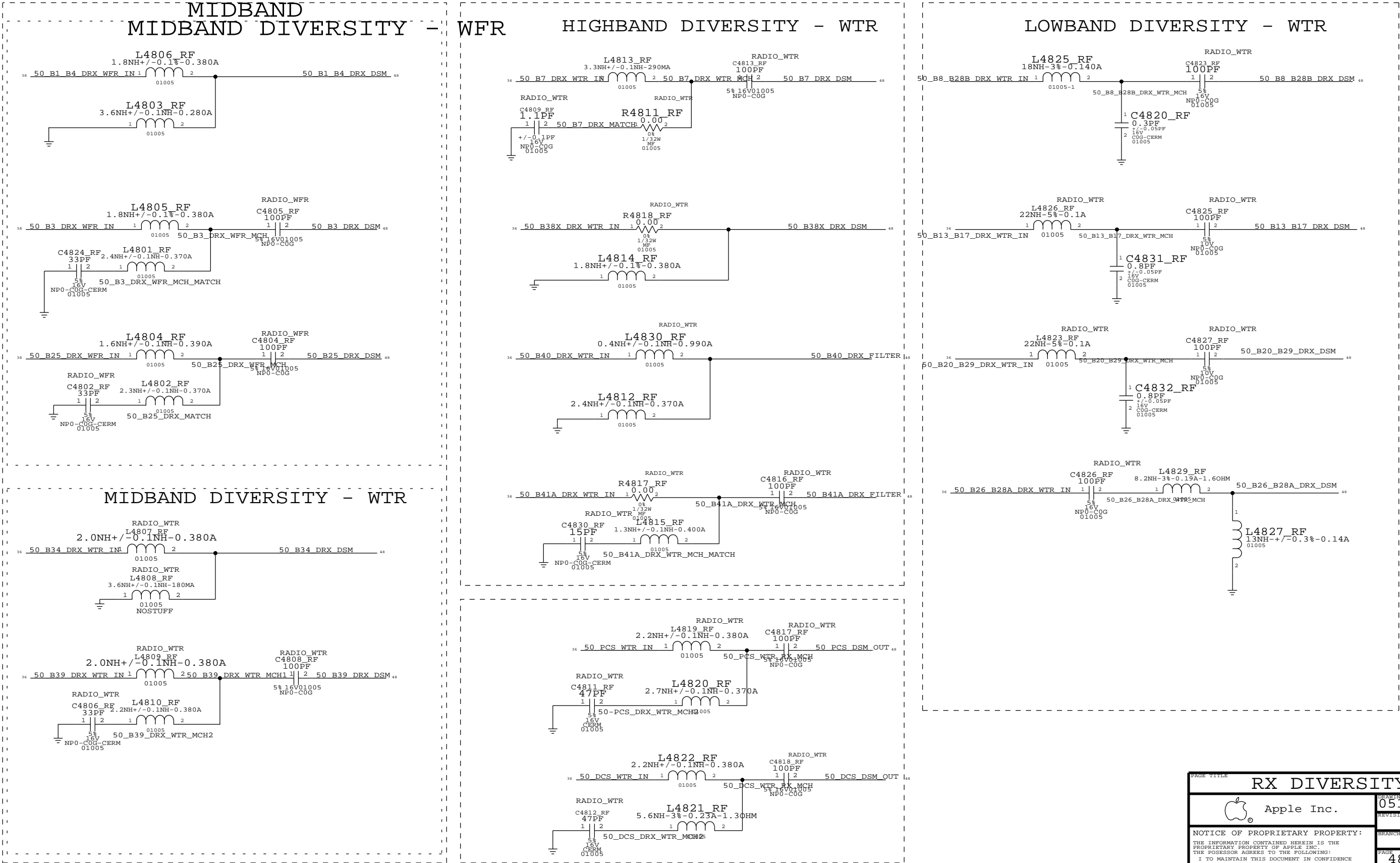


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RX DIVERSITY (1)

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L1829
U1801

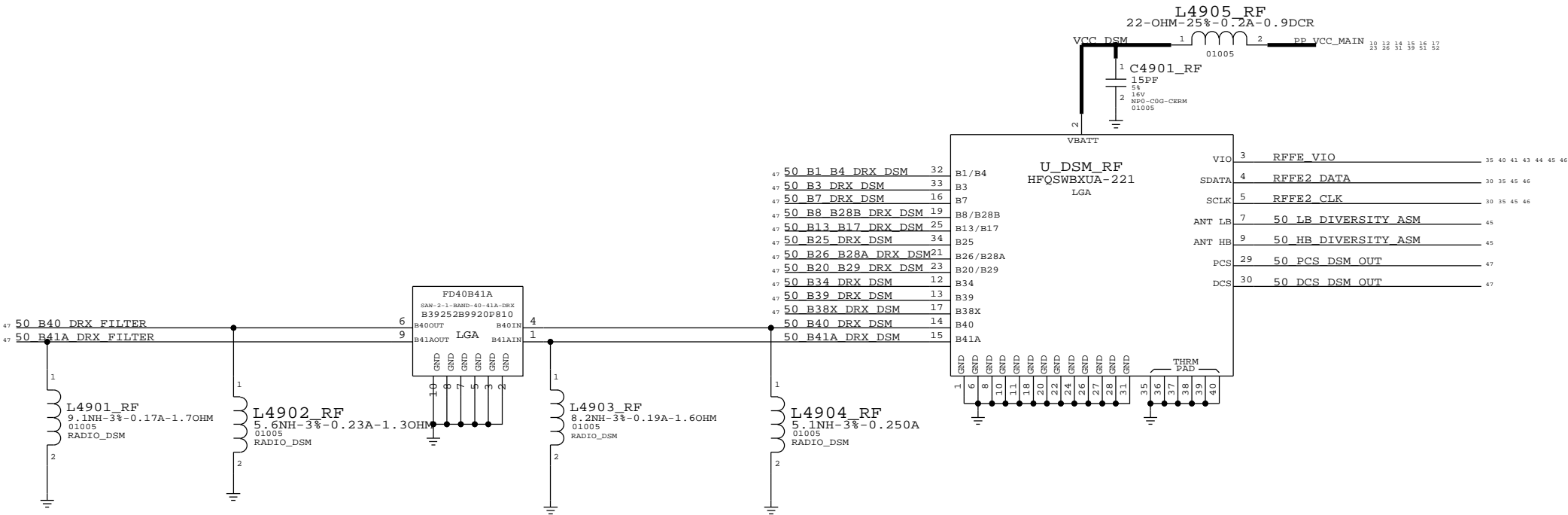



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RX DIVERSITY (2)

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C1900
R1900
L1900
U1901

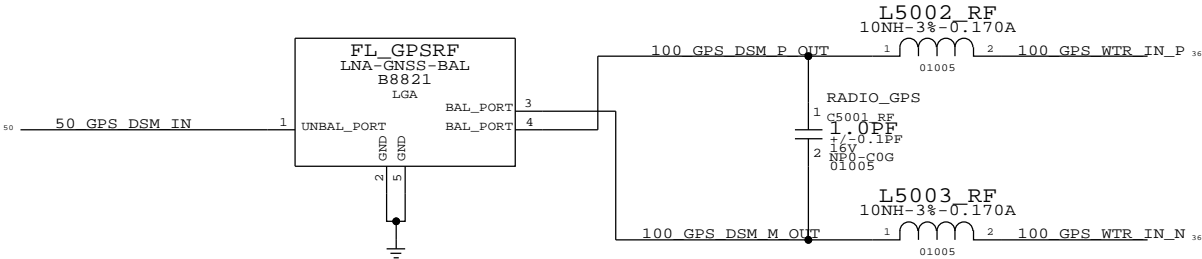



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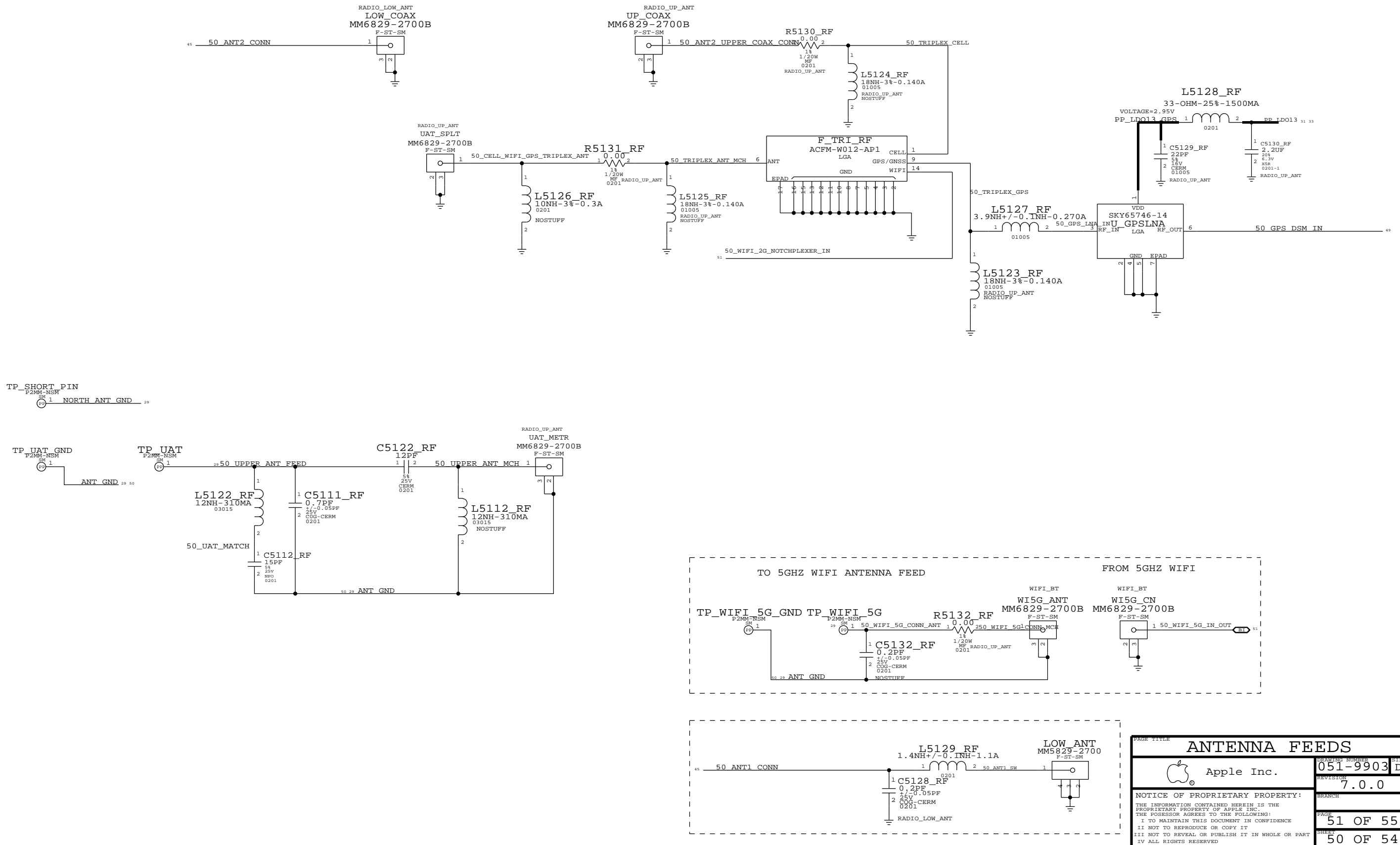
C1900
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TEST & COAX CONNECTOR FOR LOWER SECTION OF MLB



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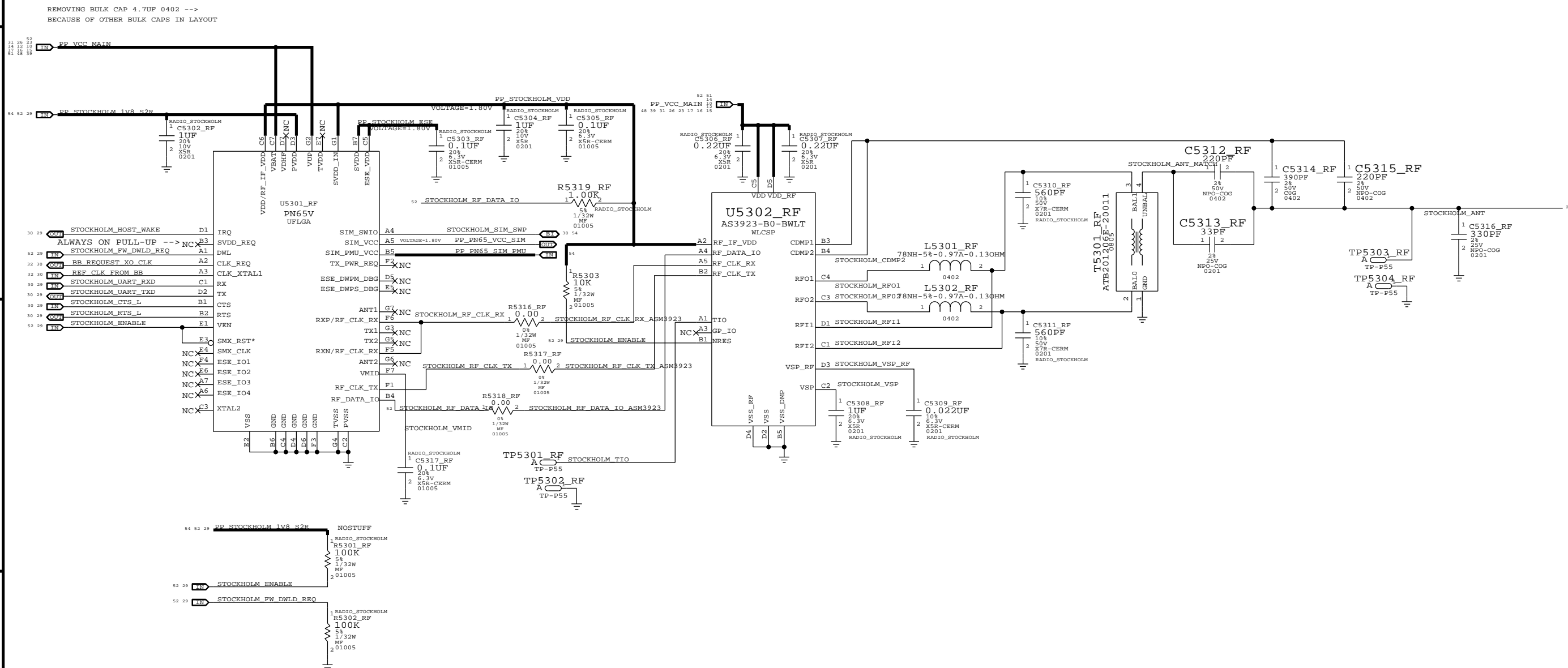



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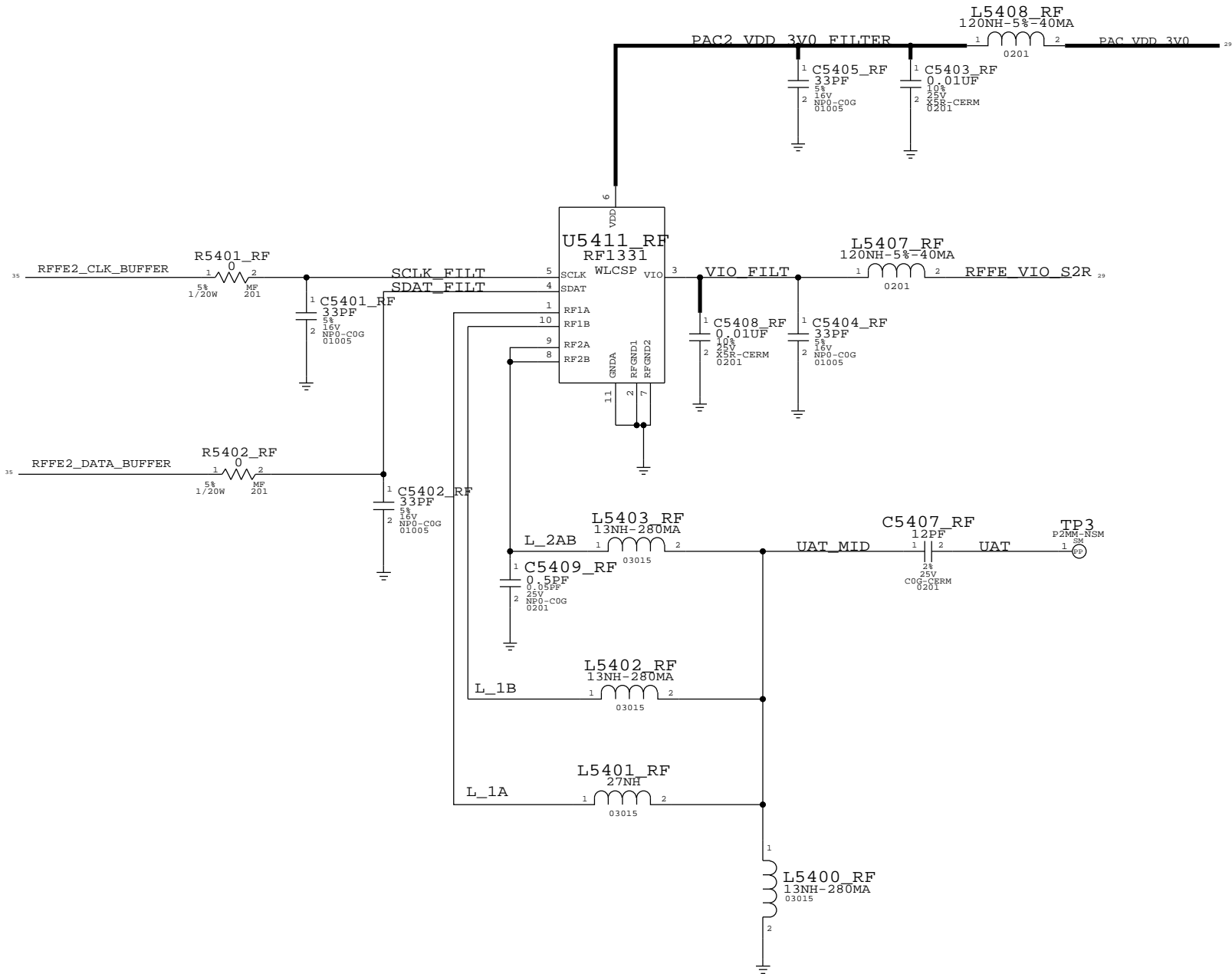
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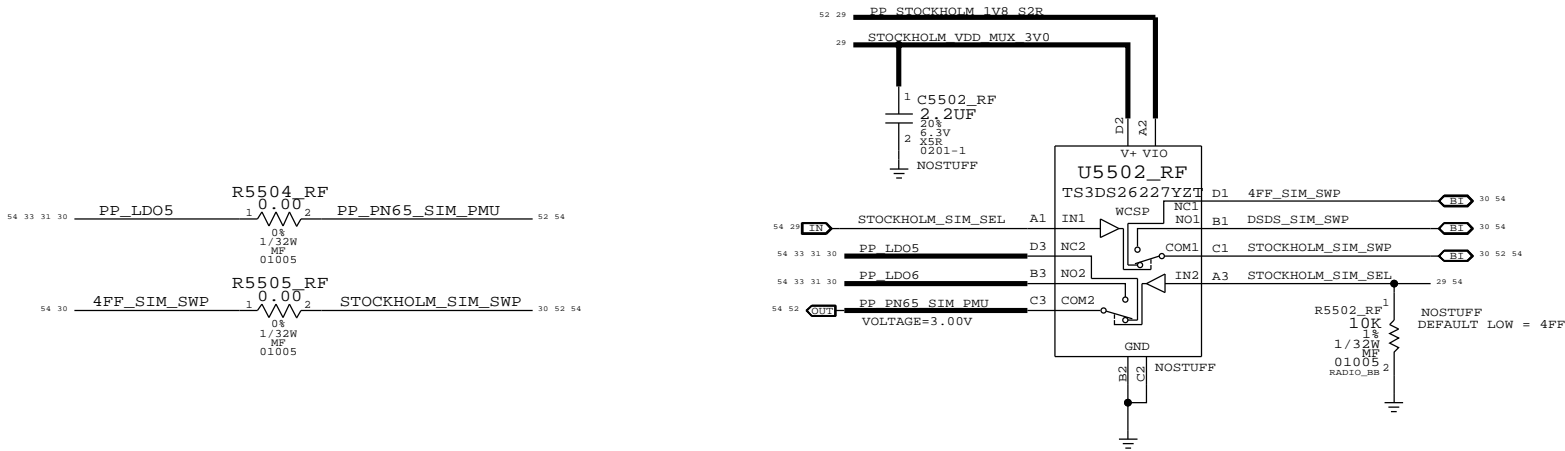
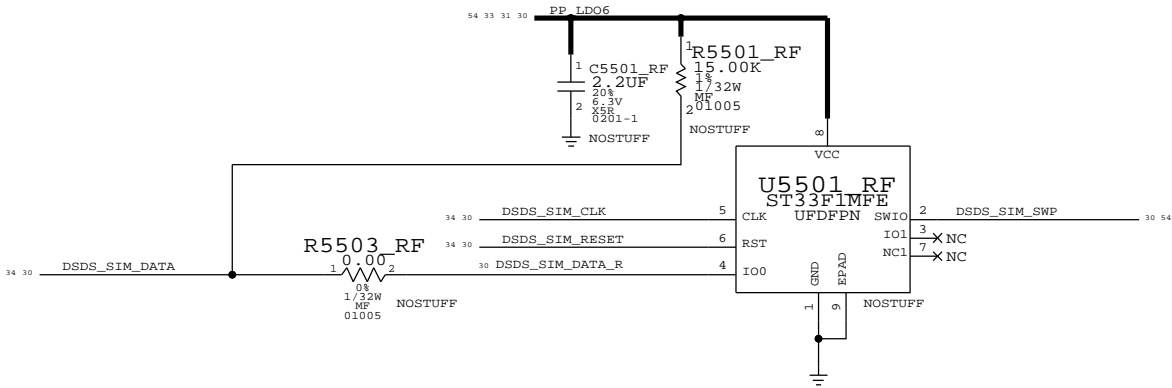
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